

# 2I640SW

**Intel Elkhart Lake ATOM®**

**x6413E / J6412 SoC CPU,**

**DDR4 SODIMM, 2 x LAN / 2 x HDMI**

**/ USB / COM / M.2**

**All-In-One SBC**

**Intel Elkhart Lake ATOM® x6413E / J6412 SoC CPU**

**2 x HDMI, eDP / LVDS, Touch Screen**

**2 x M.2, 2 x LAN, 1 x Nano SIM**

**USB, COM, Wide Range DC-IN 9~36V**

## **CAUTION**

**RISK OF EXPLOSION IF BATTERY IS REPLACED  
BY AN INCORRECT TYPE.**

**DISPOSE OF USED BATTERIES ACCORDING  
TO THE INSTRUCTIONS**

**NO. 2I640SW**

**Release date: MAR. 20. 2025**

# Contents

2I640SW	
Warning!.....	1
Hardware Notice Guide .....	2
<b>CHAPTER 1 GENERAL INFORMATION</b> .....	4
1-1 MAJOR FEATURE.....	5
1-2 SPECIFICATION .....	6
1-3 INSTALLING THE SO-DIMM .....	7
1-3-1-1 REMOVING the SO-DIMM .....	9
1-4 M.2B-KEY 2242 / 3042 PCIe / SATA-SSD / USB 2.0 .....	10
1-4-1 M.2B-KEY 2242 / 3042 PCIe 2X / USB 2.0 / USB 3.1 .....	11
<b>CHAPTER 2 HARDWARE INSTALLATION</b> .....	12
2-1 DIMENSION-2I640SW .....	12
2-2 LAYOUT-2I640SW-CONNECTOR AND JUMPER TOP .....	13
2-2-1 LAYOUT-2I640SW-CONNECTOR AND JUMPER BOT .....	14
2-3 LAYOUT-2I640SW-FUNCTION MAP TOP .....	15
2-3-1 LAYOUT-2I640SW-FUNCTION MAP BOT .....	16
2-4 DIAGRAM-2I640SW TOP .....	17
2-4-1 DIAGRAM-2I640SW BOT .....	18
2-5 FUNCTION MAP-2I640SW TOP .....	19
2-5-1 FUNCTION MAP-2I640SW BOT .....	20
2-6 LIST OF JUMPERS .....	21
2-7 JUMPER SETTING DESCRIPTION .....	21
2-8 JSB1: CMOS DATA CLEAR .....	22
2-9 JAT1: POWER IN ALWAYS ON FUNCTION .....	23
2-10 JVL1: LCD PANEL POWER SELECT .....	23
<b>CHAPTER 3 CONNECTION</b> .....	24
3-1 LIST OF CONNECTORS.....	24
3-2 CMOS BATTERY CONNECTOR .....	25
3-3 USB INTERFACE .....	25
3-4 LAN INTERFACE .....	27
3-5 COM INTERFACE .....	27
3-6 FRONT PANEL PIN HEADER .....	29
3-7 DIO INTERFACE .....	30
3-7-1 IO DEVICE: F81966 LPC DIO UNDER WINDOWS .....	31
3-7-2 IO DEVICE: F81966 LPC DIO UNDER LINUX CONSOLE .....	34
3-8 CO1: SMBus 1x4 PIN (1.25mm) WAFER .....	37
3-9 CI2C1: I2C Bus 1x4 PIN (1.25mm) WAFER .....	37
3-10 CPI1: DC POWER INPUT 1x4 PIN (2.0mm) WAFER (RED) .....	38
3-11 CPO1: +12V / +5V DC VOLTAGE OUYPUT 1x4 PIN (2.0mm) WAFER (BLACK) .....	38

3-12 CT1: TOUCH SCREEN 2x5 PIN (1.25mm) USB INTERFACE .....	39
3-13 SATA: SATA PORT 1x7 PIN CONNECTOR .....	40
3-14 SIM1: NANO SIM CARD PUSH-PUSH .....	40
3-15 NGFF1: PCI EXPRESS M.2 B KEY 2242 / 3042 H=8.5 SOCKETS 75PIN .....	41
3-16 NGFF2: PCI EXPRESS M.2 B KEY 2242 / 3042 H=8.5 SOCKETS 75PIN .....	43
3-17 DISPLAY INTERFACE .....	45
3-18 LVDS1: LVDS INTERFACE 2x15 PIN (1.25mm) WAFER .....	47
3-19 CPP1: LVDS PANEL BACKLIGHT POWER 1x5 PIN (2.0mm) WAFER .....	48
3-20 LVDS1: eDP INTERFACE 2x15 PIN (1.25mm) WAFER .....	49
3-21 CPP1: eDP PANEL BACKLIGHT POWER 1x5 PIN (2.0mm) WAFER .....	50
3-22 CONNECTOR WAFER OF COMPATIBLE BRAND AND PART NUMBER LIST .....	50
<b>CHAPTER 4 INTRODUCTION OF BIOS .....</b>	<b>51</b>
4-1 ENTER SETUP .....	51
4-2 BIOS MENU SCREEN & FUNCTION KEYS .....	52
4-3 GETTING HELP .....	53
4-4 MENU BARS .....	53
4-5 MAIN .....	54
4-6 ADVANCED .....	56
4-6-1 BOOT CONFIGURATION .....	57
4-6-2 SOC CONFIG CONFIGURATION .....	58
4-6-2-1 ACPI SETTINGS .....	59
4-6-2-2 CPU POWER LIMIT CONFIGURATION .....	60
4-6-2-3 SYSTEM AGENT(SA) CONFIGURATION .....	63
4-6-2-4 PCH-IO CONFIGURATION .....	65
4-6-2-4-1 PCI EXPRESS CONFIGURATION .....	66
4-6-2-4-2 SATA CONFIGURATION .....	68
4-6-2-5 PCH-FW CONFIGURATION .....	69
4-6-3 SIO F81966 .....	70
4-6-3-1 UART PORT 1 CONFIGURATION .....	71
4-6-3-2 UART PORT 2 CONFIGURATION .....	75
4-6-3-3 UART PORT 3 CONFIGURATION .....	78
4-6-3-4 UART PORT 4 CONFIGURATION .....	81
4-6-3-5 HARWARE MONITOR .....	84
4-6-3-6 RESTORE ON POWER LOSS .....	85
4-6-4 NVM EXPRESS INFORMATION .....	86
4-7 SECURITY .....	87
4-8 POWER .....	90
4-9 BOOT .....	93
4-10 SAVE & EXIT .....	94
4-11 HOW TO UPDATE INSYDE BIOS .....	95
APPENDIX B: RESOLUTION LIST .....	96

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User Manual edition 0.1, MAR. 2025

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## Warning !

1. Battery  
Batteries on board are consumables.  
The life time of them are not guaranteed.
2. Fanless solution with HDD  
The specification & limitation of HDD should be considered carefully when the fanless solution is implemented.
3. We will not give further notification in case of changes of product information and manual.
4. SATA interface does not support Hot SWAP function.
5. There might be a 20% inaccuracy of WDT at room temperature.
6. Please make sure the voltage specification meets the requirement of equipment before plugging in.
7. There are two types of SSD, commercial grade and industrial grade, which provide different read/write speed performance, operation temperature and life cycle. Please contact sales for further information before making orders.
8. Caution! Please notice that the heat dissipation problem could cause the MB system unstable. Please deal with heat dissipation properly when buying single MB set.
9. Please avoid approaching the heat sink area to prevent users from being scalded with fanless products.
10. If users repair, modify or destroy any component of product unauthorizedly, We will not take responsibility or provide warranty anymore.
11. DO NOT apply any other material which may reduce cooling performance onto the thermal pad.
12. It is important to install a system fan toward the CPU to decrease the possibility of overheating / system hanging up issues, or customer is suggested to have a fine cooling system to dissipate heat from CPU.

## \* Hardware Notice Guide

1. Before linking power supply with the motherboard, please attach DC-in adapter to the motherboard first. Then plug the adapter power to AC outlet.  
Always shut down the computer normally before you move the system unit or remove the power supply from the motherboard. Please unplug the DC-in adapter first and then unplug the adapter from the AC outlet.  
Please refer photo 1 as standard procedures.
2. In case of using DIRECT DC-in (without adapter), please check the allowed range for voltage & current of cables. And make sure you have the safety protection for outer issues such as short / broken circuit, overvoltage, surge, lightning strike.
3. In case of using DC-out to an external device, please make sure its voltage and current comply with the motherboard specification.
4. The total power consumption is determined by various conditions (CPU / motherboard type, device, application, etc.). Be cautious to the power cable you use for the system, one with UL standard will be highly recommended.
5. It's highly possible to burn out the CPU if you change / modify any parts of the CPU cooler.
6. Please wear wrist strap and attach it to a metal part of the system unit before handling a component. You can also touch an object which is ground connected or attached with metal surface if you don't have wrist strap.
7. Please be careful to handle & don't touch the sharp-pointed components on the bottom of PCBA.
8. Remove or change any components from the motherboard will VOID the warranty of the motherboard.
9. Before you install / remove any components or even make any jumper setting on the motherboard, please make sure to disconnect the power supply first.  
(follow the aforementioned instruction guide)
10. "POWERON after PWR-Fail" function must be used carefully as below:  
When the DC power adaptor runs out of power, unplug it from the DC current;  
Once power returns, plug it back after 5 seconds.  
If there is a power outage, unplug it from the AC current, once power returns, plug it back after 30 seconds. Otherwise it will cause system locked or made a severe damage.

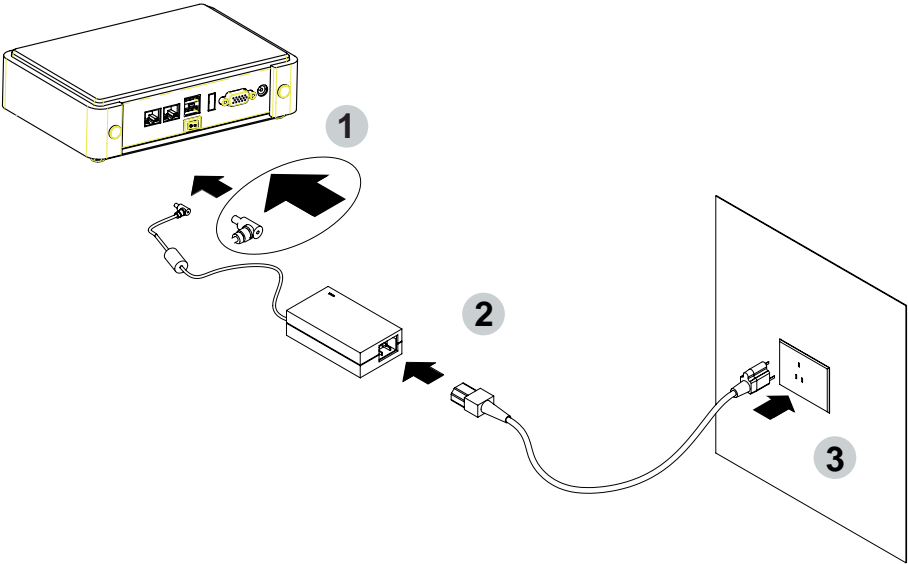
### **Remark 1:**

**Always insert / unplug the DC-in horizontally & directly to / from the motherboard. DO NOT twist, it is designed to fit snugly.**

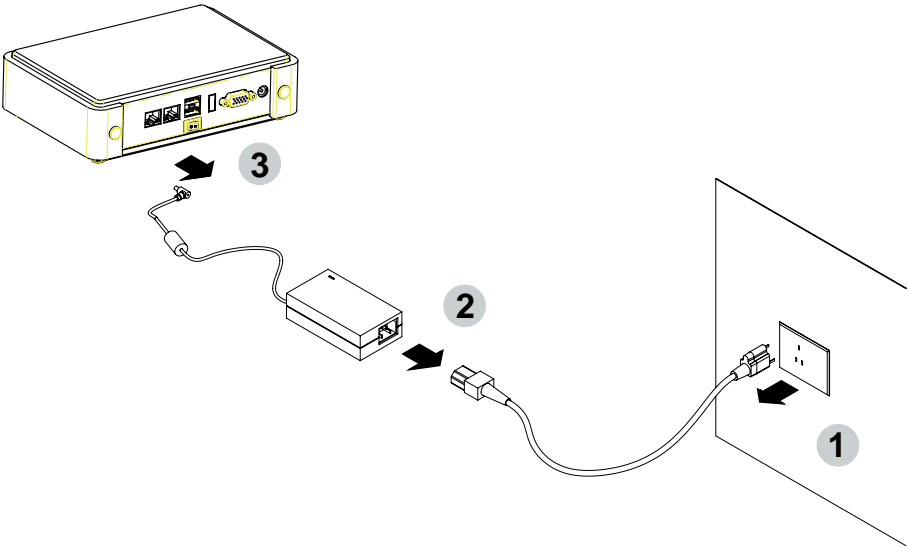
**Moreover, erratic pull / push action might cause an unpredictable damage to the component & system unit.**

**Photo 1**

**Insert**



**Unplug**



---

# Chapter-1

## General Information

The 2I640SW is a 2.5" (110 x 98 mm) motherboard powered with Intel Atom®x6000E series and Celeron® J (formerly Elkhart Lake) processor & offered the ideal platform for high performance applications. The ultra compact (110 x 98 mm) motherboard with wide range 9~36V DC power input & embeds multiple Intel 2.5GbE LAN, USBs, COM Ports and HDMI display interface that offer the ideal platforms for high performance applications in Networking, Smart Automation, Machine Vision, In-vehicle, Industry 4.0 and any compact high-performance Internet of Things (IoT) applications.

The 2I640SW supports high-speed data transfer interfaces such as PCIe gen3, USB 3.0, and SATA 6 Gb/s (SATA III) for SATA interface M.2 B-Key device, with one-channel DDR4 3200 MHz memory up to 32 GB SODIMM slot and supports 4 serial ports RS232 / RS485 / RS422 jumper free auto switch by BIOS settings. It supports 2 port of USB 3.0, 2 port of USB 2.0. The expandable interfaces include 1 M.2 B-Key for PCIe x 2, USB 3.0 / 2.0 interface, and 1 M.2 B-Key SATA, PCIe and USB interface. 2I640SW supports 1 LVDS & eDP interface selectable by BIOS for LCD Panel and 1 panel inverter power for panel dimming control. It suitable for ALL-IN ONE Panel PC, POS Kiosk and automation control systems.

The embedded motherboard 2I640SW is specially designed with Wide-Range Voltage DC in (9~36V) for widely varying input voltage requirement. All wafer IO design offers superb performance and PC specification in the industry using the specific housing. It supports with two 2.5Gbps Ethernet for seamless broadband connectivity. With Wake-On LAN function and the PXE function in BIOS, these are perfect control boards for networking devices.

The All-In-One motherboard 2I640SW is fully compatible with industry standards, plus technical enhancements and thousands of software applications developed for IBM PC/AT compatible computers. These control logic provides high-speed performance for the most advanced multi user and multitasking applications available today.



## 1-1 Major Feature

1. Intel® Atom x6413E Processor 1.5GHz / 2.7GHz (Quad Core),  
Intel® Celeron Processor J6412 2.0GHz / 2.6GHz (Quad Core)
2. Intel® UHD Graphics for 10th Gen Intel® Atom x6413E 500MHz / 750MHz,  
Intel® Celeron J6412 400MHz / 800MHz
3. Support LVDS 2 Channels 48bits, Max up to 1920 x 1080 resolution,  
DVI / VGA up to 1920 x 1200, LVDS / eDP selectable by BIOS settings
4. Support USB Touch & backlight power control function
5. DDR4 SODIMM slot x 1, up to 32GB
6. Support 2 x 2.5 Gbps Intel LAN ports.
7. Support 4 x RS232 selectable to RS485 / RS422 by BIOS
8. 2 x USB 3.0 and 5 x USB 2.0
9. Support extended 1 x M.2 B-Key for PCIe x 2 and USB 3.0 / 2.0 interface  
with Nano SIM, 1 x M.2 B-Key for SATA, PCIe and USB interface.
10. Hardware digital Input & Output, 4 x DI / 4 x DO, Hardware Watch Dog Timer,  
0~255 sec programmable
11. Wide Range DC IN +9V~36V

## 1-2 Specification

1. **SOC:** Intel® Atom x6413E Processor 1.5GHz / 2.7GHz (Quad Core),  
Intel® Celeron Processor J6412 2.0GHz / 2.6GHz (Quad Core)
2. **Memory:** DDR4 SODIMM slot x 1, up to 32GB
3. **Graphics:** Intel® UHD Graphics for 10th Gen Intel® Atom x6413E 500MHz / 750MHz,  
Intel® Celeron J6412 400MHz / 800MHz. Support LVDS 2 Channels 48bits,  
Max up to 1920 x 1080 resolution, HDMI 1.4b up to 3840 x 2160, DVI / VGA up  
to 1920 x 1200
4. **Touch:** USB Touch
5. **SATA:** Integrated Serial ATA Host Controller 1 SATA port,  
SATA Gen3 Data transfer rates up to 6.0 Gb/s (600 MB/s).
6. **LAN:** 2 Intel I226-IT LAN chipset with 10 / 100 / 1000 / 2500 Mbps for PCIe x 1 V2.1
7. **I/O Chip:** Switch chipset for 4 port RS232 / RS422 / RS485 selected by BIOS
8. **USB:** 2 type A USB 3.0, 2 USB 2.0, 3 USB 2.0 internal
9. **WDT/DIO:** Hardware digital Input & Output, 4 x DI / 4 x DO (Option) /  
Hardware Watch Dog Timer, 0~255 sec programmable
10. **Expansion interface:** one M.2 B-Key for PCIe x 2 and USB 3.0 / 2.0 interface  
with Nano SIM, one M.2 B-key for SATA, PCIe and USB interface
11. **BIOS:** Insyde UEFI BIOS
12. **Dimension:** 110 x 98 mm
13. **Power:** On board DC +9~36V

## 1-3 Installing the SO-DIMM

1. Align the SO-DIMM with the connector at a 45 degree angle.

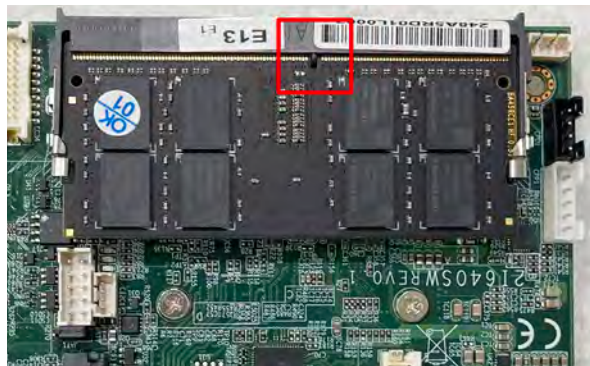


2. Press the SO-DIMM into the connector until you hear a click.

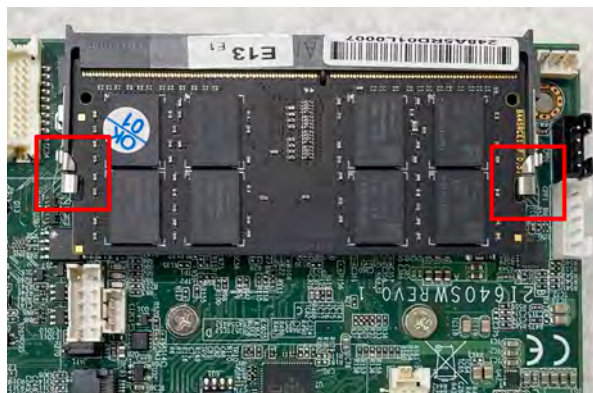


Notices:

1. The connectors are designed to ensure the correct insertion. If you feel resistance, check the connectors & golden finger direction, and realign the card.

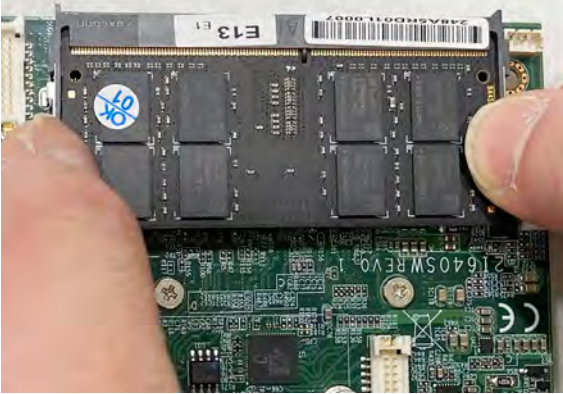


2. Make sure the retaining clips (on two sides of the slot) lock onto the notches of the card firmly.



### 1-3-1-1 Removing the SO-DIMM

1. Release the SO-DIMM by pulling outward the two retaining clips and the SO-DIMM pops up slightly.

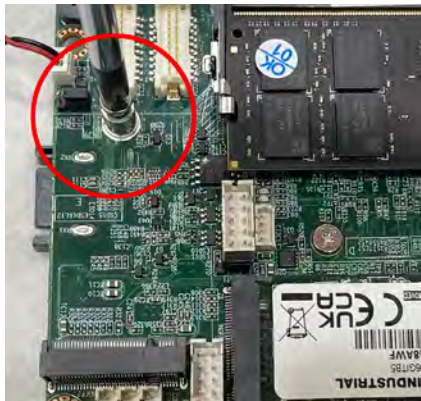


2. Lift the SO-DIMM out of its connector carefully.



## 1-4 M.2B-Key 2242 / 3042 PCIe / SATA-SSD / USB2.0

1. Unscrew the screw on the board



2. Plug in the Mini Card in a 45 angle



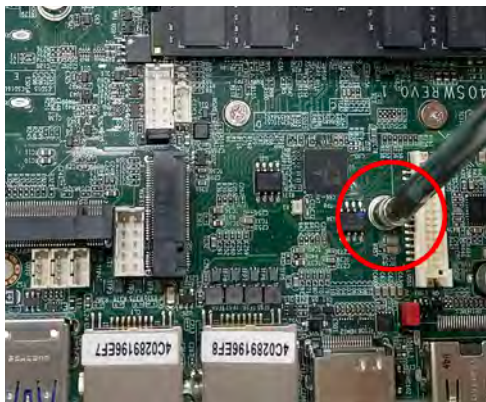
3. Gently push down the Mini Card and screw the screw back.





## 1-4-1 M.2B-Key 2242 / 3042 PCIe 2X / USB 2.0 / USB 3.1

1. Unscrew the screw on the board



2. Plug in the Mini Card in a 45 angle

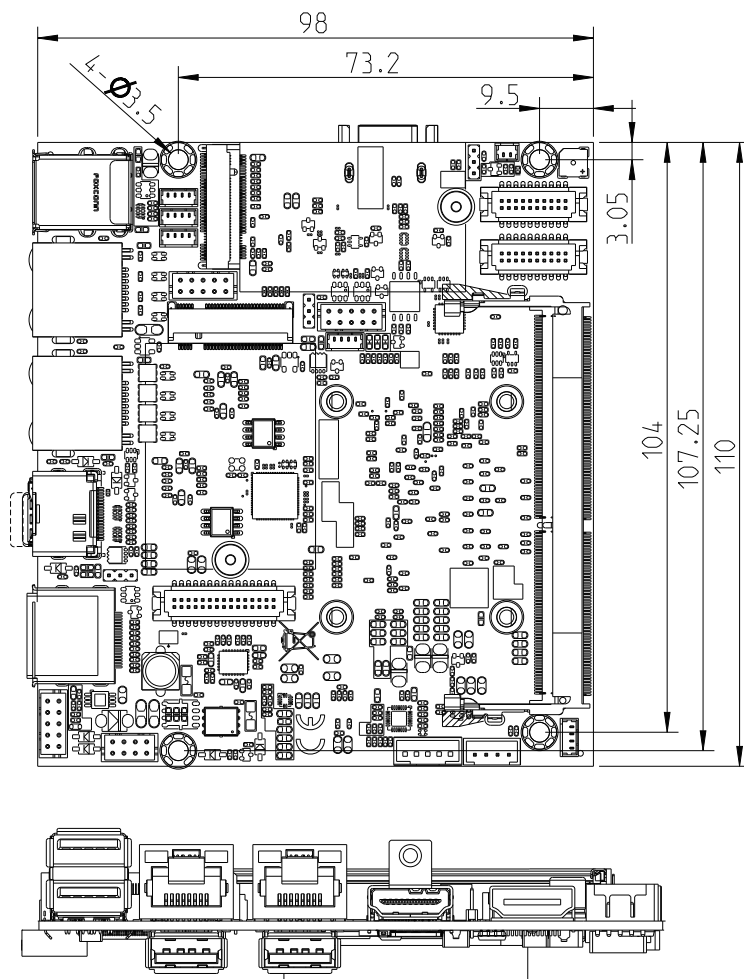


3. Gently push down the Mini Card and screw the screw back.



# Chapter-2

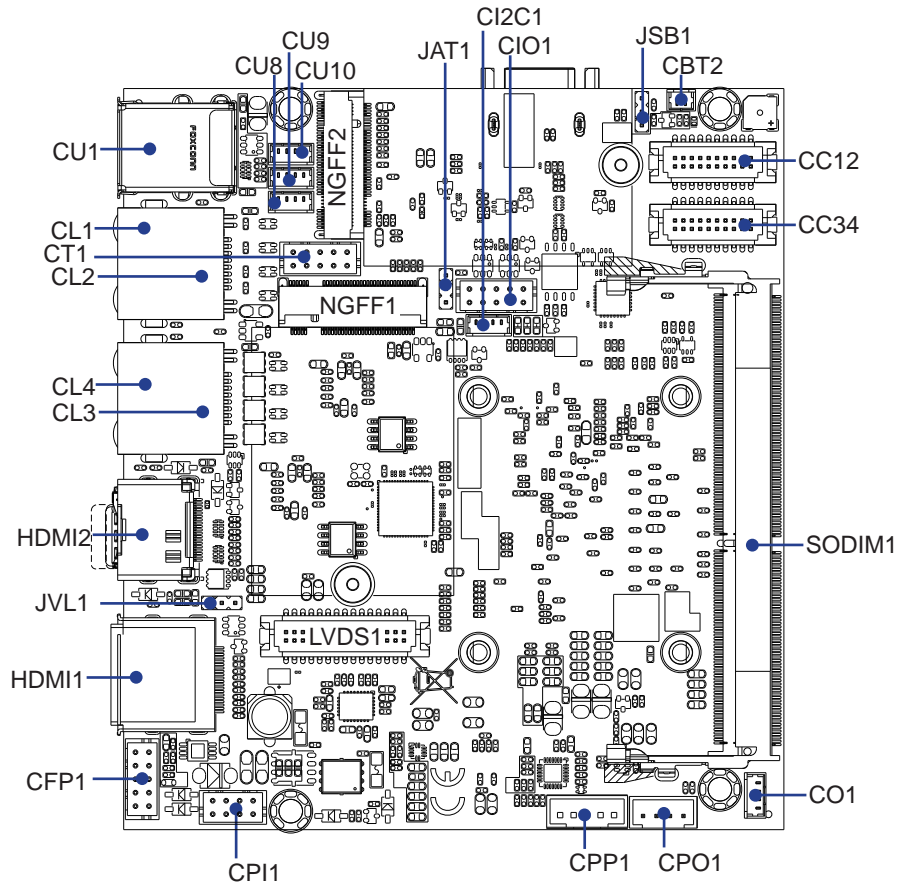
## 2-1 Dimension-2I640SW



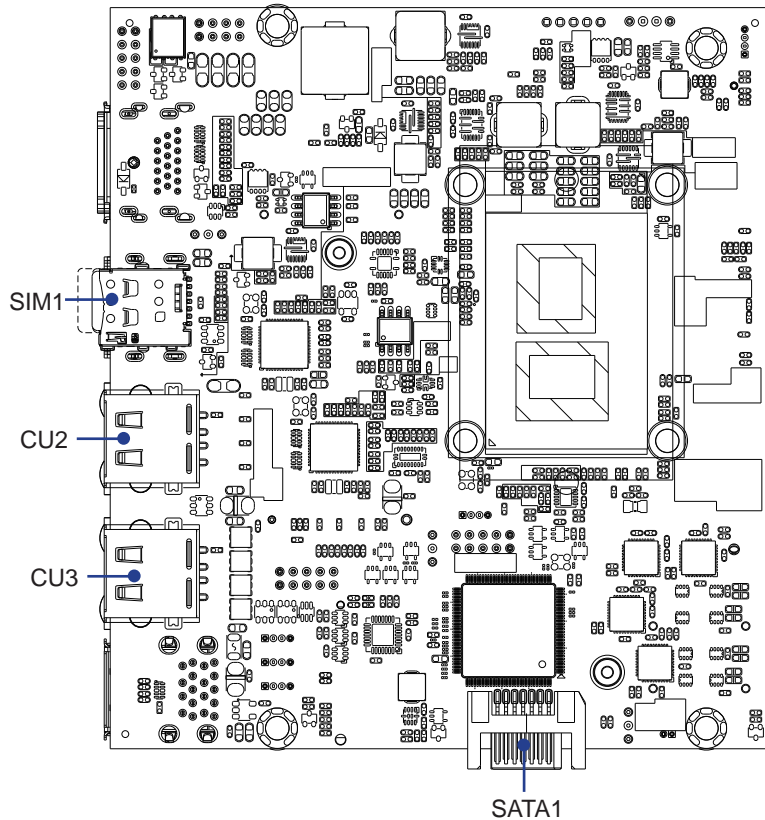


## 2-2 Layout-2I640SW-Connector and Jumper

TOP

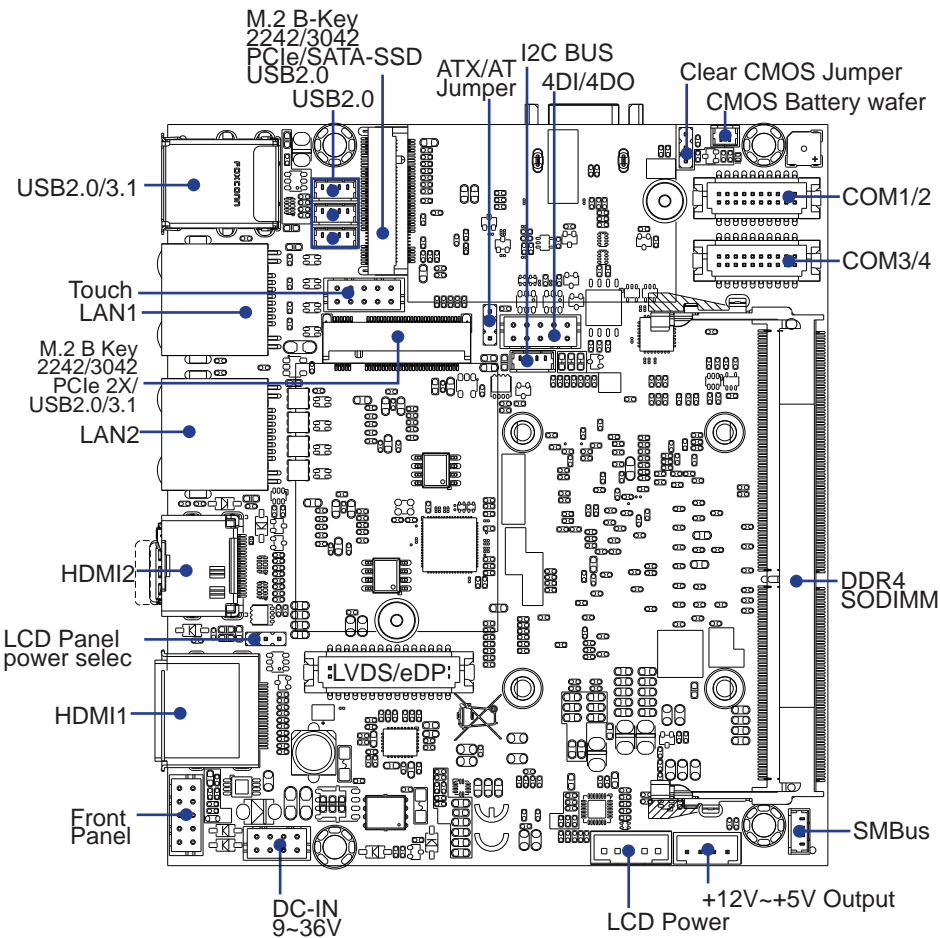


**2-2-1 Layout-2I640SW-Connector and Jumper Bottom**  
**BOT**



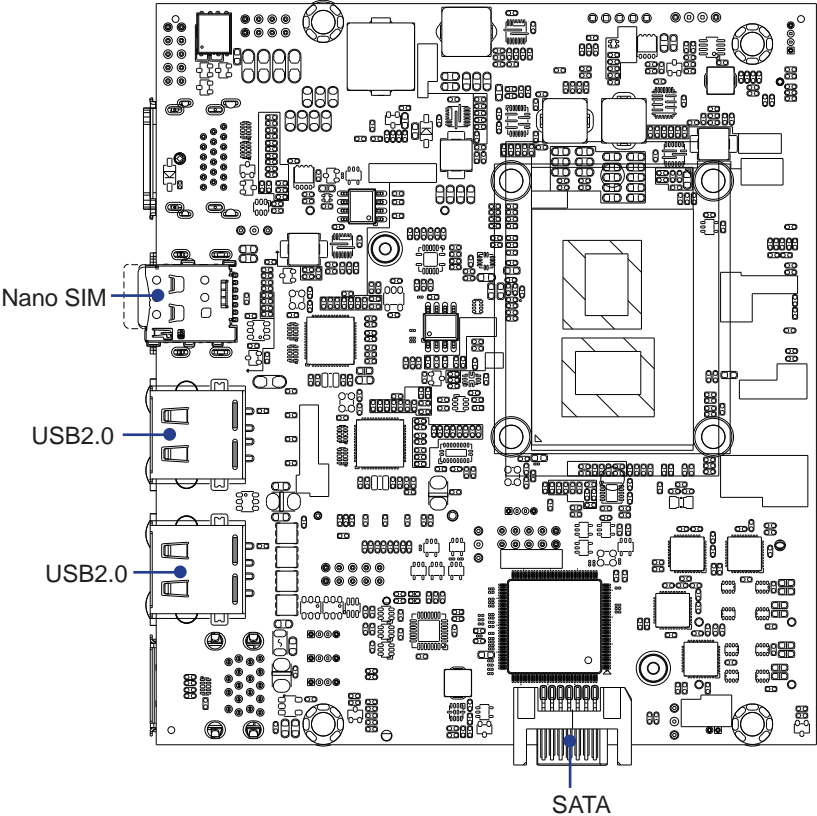
## 2-3 Layout-2I640SW-Function MAP

TOP



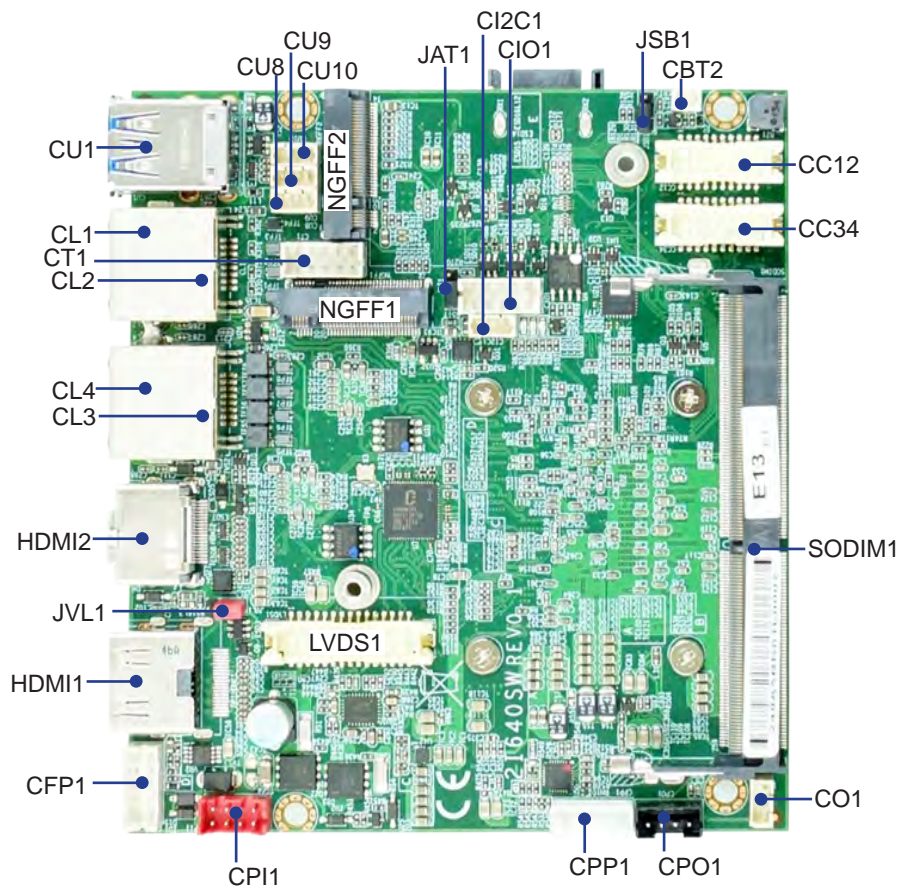
2-3-1 Layout-2I640SW-Function MAP

BOT

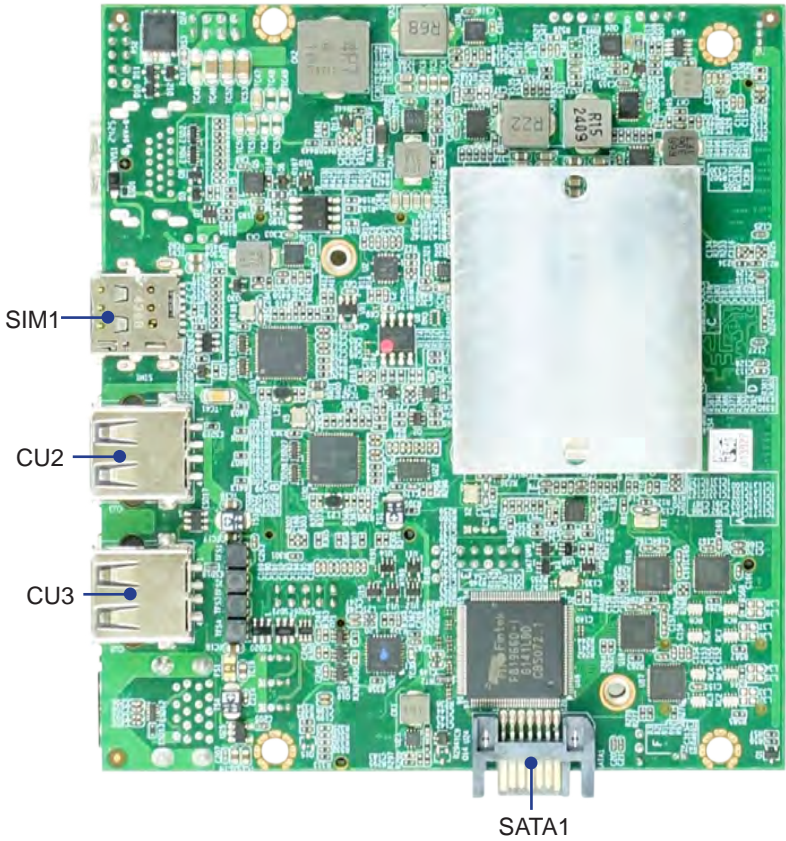


2-4 Diagram- 2I640SW

TOP



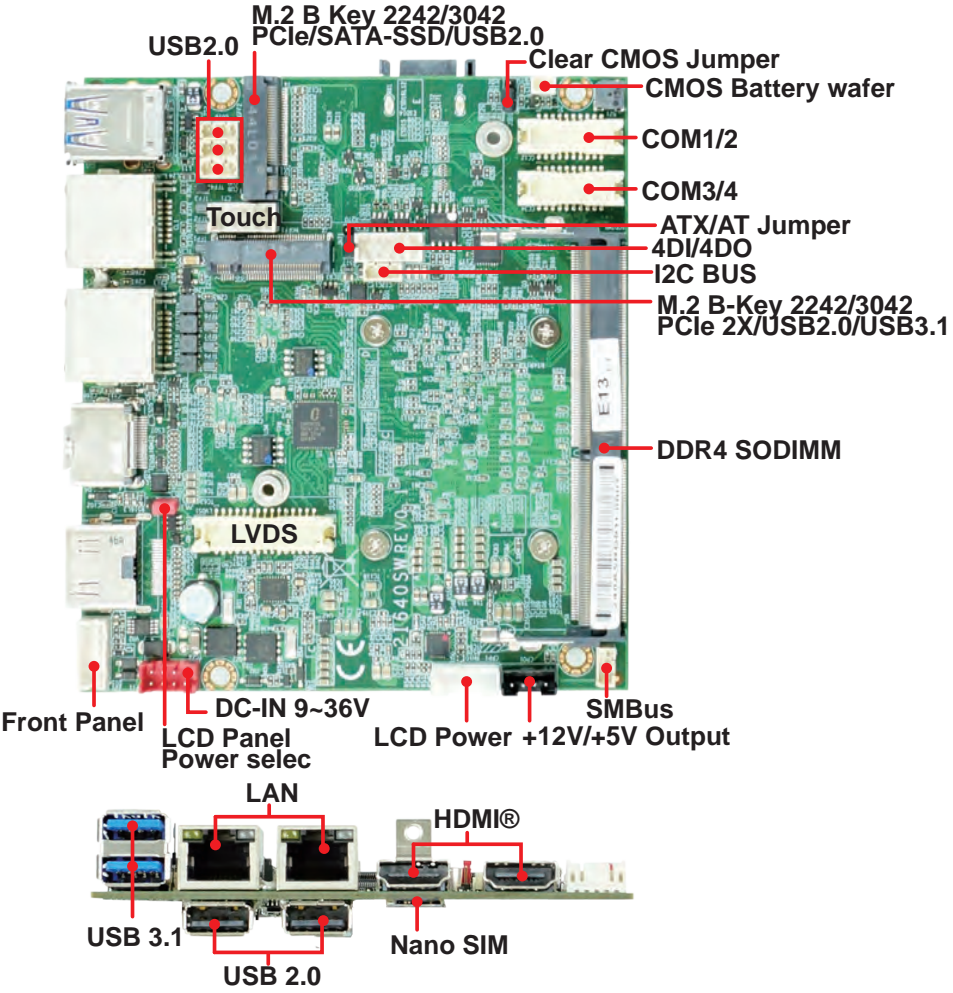
**2-4-1 Diagram- 2I640SW**  
**BOT**



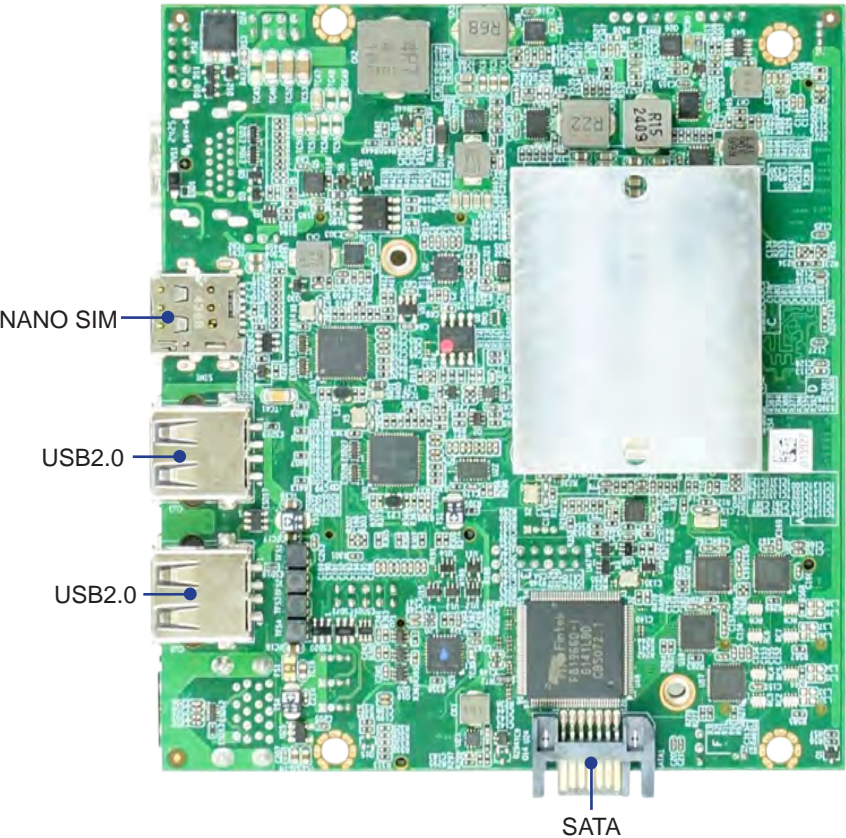


2-5 Function MAP- 2I640SW

TOP



**2-5-1 Function MAP- 2I640SW**  
**BOT**





## 2-6 List of Jumpers

- JSB1: CMOS DATA Clear
- JAT1: Power in always ON function
- JVL1: LCD panel power select

## 2-7 Jumper Setting Description

A jumper is ON as a closed circuit with a plastic cap covering two pins. A jumper is OFF as an open circuit without the plastic cap. Some jumpers have three pins, labeled 1, 2, and 3. You could connect either pin 1 and 2 or 2 and 3.

The below figure 2.2 shows the examples of different jumper settings in this manual.

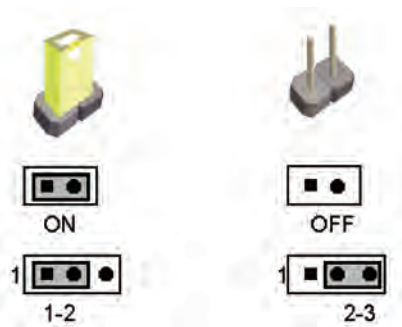


Figure 2.2

All jumpers already have its default setting with the plastic cap inserted as ON, or without the plastic cap as OFF. The default setting may be referred in this manual with a " \* " symbol .

## 2-8 JSB1: CMOS DATA Clear

A battery must be used to retain the motherboard configuration in CMOS RAM.  
Close Pin1 and pin 2 of JSB1 to store the CMOS data.

To clear the CMOS, follow the procedures below:

- 1. Turn off the system and unplug the AC power
- 2. Remove DC IN power cable from DC IN power connector
- 3. Locate JSB1 and close pin 1-2 for few seconds
- 4. Return to default setting by Close pin 1-2
- 5. Connect DC IN power cable back to DC IN Power connector

JSB1	DESCRIPTION
*1-2	Normal set
2-3	CMOS data clear

Note: Do not clear CMOS unless

- 1. *Troubleshooting*
- 2. *Forget password*
- 3. *You fail over-clocking system*



JSB1



\*Normal

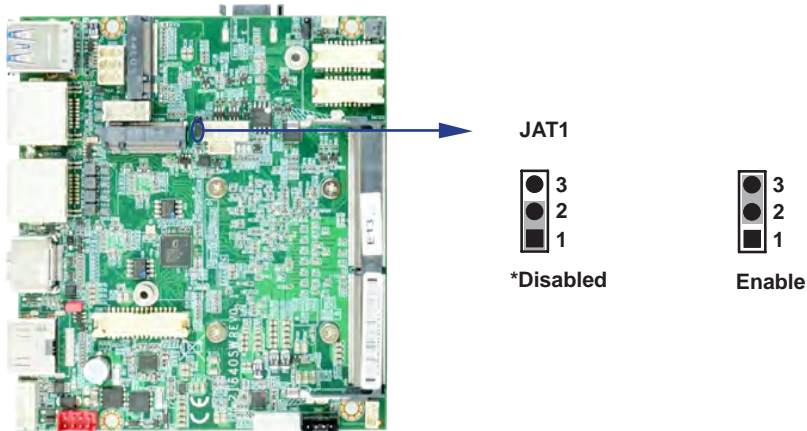


CMOS

## 2-9 JAT1: Power in always ON function

JAT1	DESCRIPTION
*1-2	Disabled
2-3	Enable

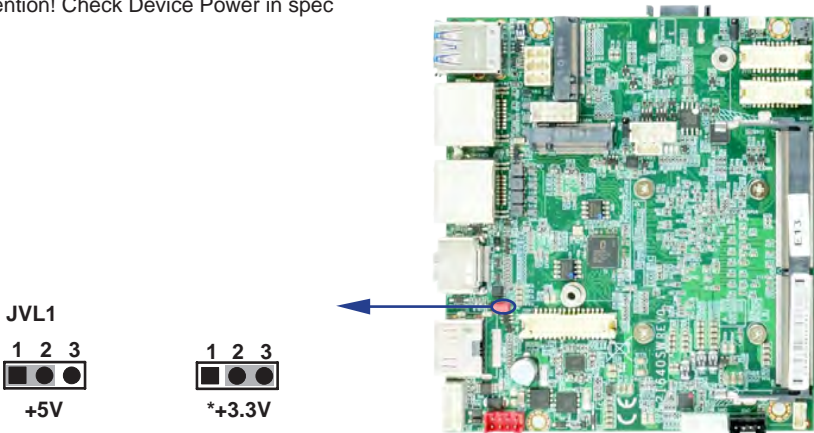
NOTE: Power always on function default is disabled.



## 2-10 JVL1: LCD panel power select

JVL1	DESCRIPTION
1-2	+5V
*2-3	+3.3V

Note: Attention! Check Device Power in spec



# Chapter-3

## Connection

This chapter provides all necessary information of the peripheral's connections, switches and indicators. Always power off the board before you install the peripherals.

### 3-1 List of Connectors

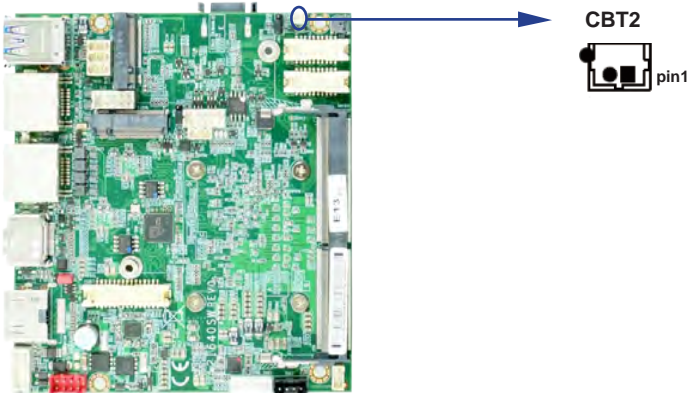
CBT2:	CMOS Battery in 1x2 pin (1.25mm) wafer
CU1:	Dual USB 3.0 type A connector
CU2:	USB 2.0 type A connector
CU3:	USB 2.0 type A connector
CU8:	USB 2.0 port 1x4 pin (1.25mm) wafer
CU9:	USB 2.0 port 1x4 pin (1.25mm) wafer
CU10:	USB 2.0 port 1x4 pin (1.25mm) wafer
CL1:	RJ45 LAN connector
CL4:	RJ45 LAN connector
CL2:	LAN port 2x4 pin (2.0mm) wafer (option)
CL3:	LAN port 2x4 pin (2.0mm) wafer (option)
CC12:	COM1/2 2x10 pin (1.25mm) wafer
CC34:	COM1/2 2x10 pin (1.25mm) wafer
CFP1:	Front Panel connector 2x5 pin (2.0mm) wafer
CIO1:	4DI/4DO 2x5 pin (2.0mm) wafer
CO1:	SMBus 1x4 pin (1.25mm) wafer
CI2C1:	I2C Bus 1x4 pin (1.25mm) wafer
CPI1:	DC-IN 2x4 pin (2.0mm) Redwafer
CPO1:	+12V / +5V output 1x4 pin (2.0mm) Black wafer
CT1:	Touch 2x5 pin (2.0mm) wafer
SATA1:	SATA connector 7pin
SIM1:	Nano SIM card socket
NGFF1:	M.2 B key 2242 / 3042 H=8.5 sockets 75pin
NGFF2:	M.2 B key 2242 / 3042 H=8.5 sockets 75pin
HDMI1:	HDMI typeA connector
HDMI2:	HDMI typeA connector
LVDS1:	LVDS 2CH / eDP 2x15 pin (1.25mm) wafer
DP1:	Display Port connector (option)
CPP1:	LVDS / ePD Panel Backlight power 1x5 pin (2.0mm) wafer

### 3-2 CMOS battery connector

- CBT2: CMOS Battery in 1x2 pin (1.25mm) wafer

PIN NO.	DESCRIPTION
1	Battery in (GND)
2	Battery in (+3V)

Note: NOTE: CBT2 for external connector can extend battery capacity.



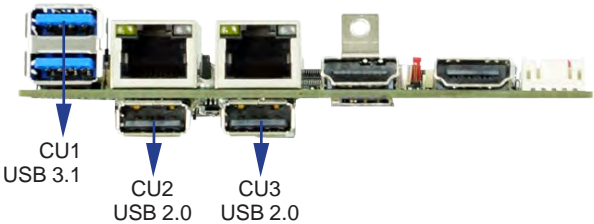
### 3-3 USB Interface

- CU1: Dual USB 3.0 / 2.0 Type A connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
		1	USB 3.0 TX+
1	+5V		
2	USB 2.0 D-	2	USB 3.0 TX-
		3	GND
3	USB 2.0 D+	4	USB 3.0 RX+
4	GND		
		5	USB 3.0 RX-

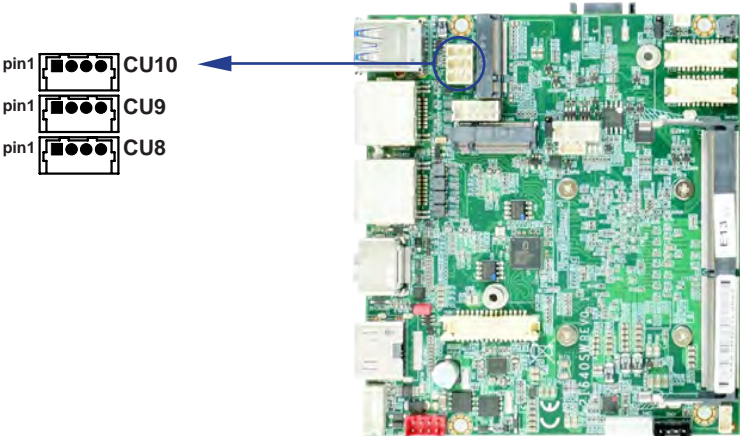
• **CU2. CU3: USB 2.0 Type A Connector**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+5V	2	DATA-
3	DATA+	4	GND



• **CU8 / CU9 / CU10: USB 2.0 1x4 pin (1.25mm) wafer**

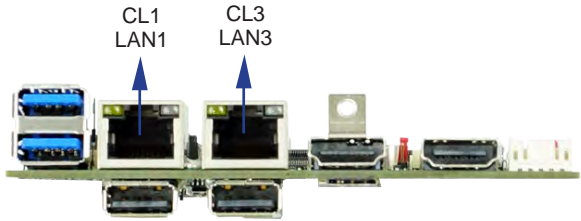
PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+5V	2	DATA-
3	DATA+	4	GND



### 3-4 LAN Interface

- **L1 / CL4: RJ45 LAN Connector**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	TD0+ / TX+	2	TD0- / TX-
3	TD1+ / RX+	4	TD2+ / NC
5	TD2- / NC	6	TD1- / RX-
7	TD3+ / NC	8	TD3- / NC



- **CL2 / CL3: LAN signal out 2x4 pin (2.0mm) wafer (option)**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	TR0-	2	TR0+
3	TR2+	4	TR1+
5	TR1-	6	TR2+
7	TR3-	8	TR3+

### 3-5 COM interface

- **CC12 / CC34: COM1 / 2 / 3 / 4 2x10 pin (1.25mm) wafer**

- **(RS232 Mode)**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	NC	2	NC
3	DCD1/3	4	DCD2/4
5	DSR1/3	6	DSR2/4
7	RXD1/3	8	RXD2/4
9	RTS1/3	10	RTS2/4
11	TXD1/3	12	TXD2/4
13	CTS1/3	14	CTS2/4
15	DTR1/3	16	DTR2/4
17	RI1/3	18	RI2/4
19	GND	20	GND

Note:

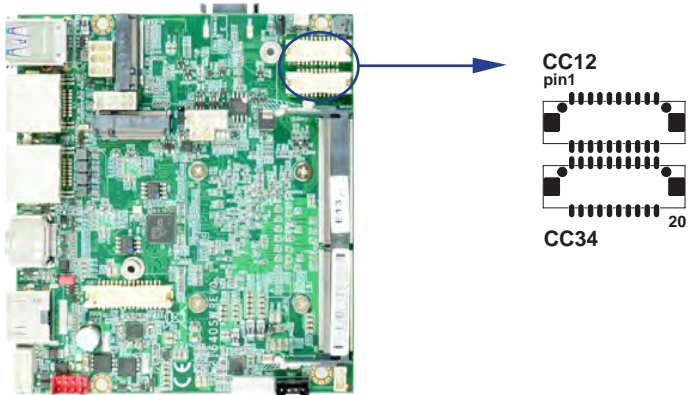
- 1. COM 1/2/3/4 Default RS232, RS485 / RS422 by BIOS control.
- 2. The pin17 / 18 RI can be modify to Power to supply device.  
The power voltage can be set +12V or +5V.  
The RI change Voltage function set by BOM control .Default is RI signal.

● RS485 Mode

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	NC	2	NC
3	RS485_Data-	4	NC
5	NC	6	NC
7	RS485_Data+	8	NC
9	NC	10	NC
11	NC	12	NC
13	NC	14	NC
15	NC	16	NC
17	NC	18	NC
19	GND	20	GND

● RS422 Mode

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	NC	2	NC
3	RS422_TX-	4	NC
5	NC	6	NC
7	RS422_TX+	8	NC
9	NC	10	NC
11	RS422_RX+	12	NC
13	NC	14	NC
15	RS422_RX-	16	NC
17	NC	18	NC
19	GND	20	GND

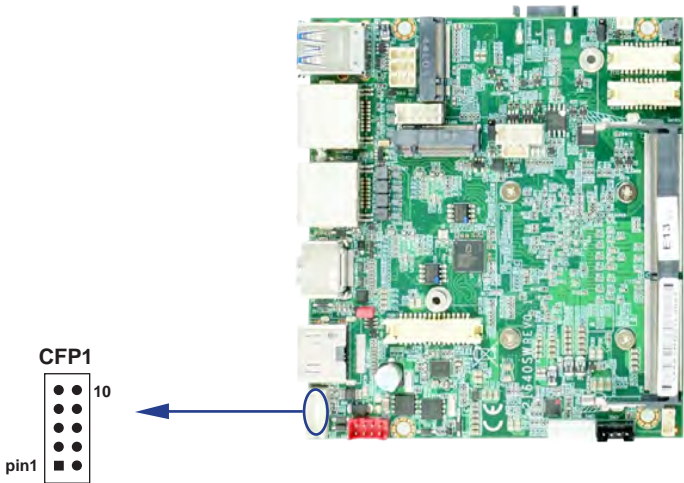




### 3-6 Front Panel Pin Header

• CFP1: Front Panel 2x5 pin (2.0mm) wafer

PIN NO.	Description	PIN NO.	Description
1	Power button pin	2	Power button GND
3	Reset pin	4	Reset GND
5	Power LED-	6	Power LED+
7	HDD LED-	8	HDD LED+
9	LAN LED-	10	LAN LED+



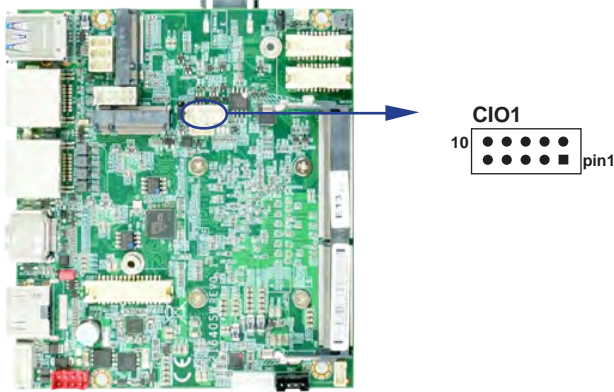
### 3-7 DIO Interface

● CIO1: DIO 0~3 2x5 pin (2.0mm) wafer

PIN NO.	Description	PIN NO.	Description
1	DI-0	2	DO-3
3	DI-1	4	DO-2
5	DI-2	6	DO-1
7	DI-3	8	DO-0
9	GND	10	+5V

Note:

1. DI pin default pull up 10K $\Omega$  to +5V.
2. If use need isolate circuit to control external device.



● WDT For F81966D-I watch dog timer device:

DC spec:

Input low Voltage (VIL): +0.8 Max

Input High Voltage(VIH): +2V Min

Output low Current (IOL): 10mA (Min) VOL=0.4V

Output High Current (IOH): -10mA (Min) VOH=2.4V

Watch Dog Time value 0~255 sec

The system will be issued reset. When WDT is enable the hardware start down counter to zero.

The reset timer have 10~20% tolerance upon the Temperature.

Note: If want to SDK support. Please contact to sales window.

# 3-7-1 IO Device: F81966 LPC DIO under Windows

## The Sample code source you can download from

[http://tprd.info/lexwiki/index.php/IO\\_Device:F81966\\_LPC\\_DIO\\_under\\_Windows](http://tprd.info/lexwiki/index.php/IO_Device:F81966_LPC_DIO_under_Windows)

Source file: F81966\_LPC\_GPIO\_UTILITY\_v1.5\_Src

Binary file: F81966\_LPC\_GPIO\_UTILITY\_v1.5.1\_Bin\_x86 F81966\_LPC\_GPIO\_UTILITY\_v1.5.1\_Bin\_x64

F81966 DLL : F81966\_DLL\_v1.1\_x86 F81966\_DLL\_v1.1\_x64

## Introduction F81966 DIO



1. Start test Button, Send bits one by one and one by one receive
2. CIO116 mode for CIO116 module use
3. Set time countdown, If the end of the countdown, the trigger signal to reboot
4. Enable loop, Continuously reset the WDT to ensure that when the system is normal, the restart signal will not be triggered.

## F81966\_DLL Function

```

F81966_DLL_API bool F81966_LPC_Init(pF81966_status status);

F81966_DLL_API BYTE F81966_LPC_Digital_Read_LOW();
F81966_DLL_API void F81966_LPC_Digital_Write_LOW(BYTE byteValue);
F81966_DLL_API BYTE F81966_LPC_Digital_Read_HIGH();
F81966_DLL_API void F81966_LPC_Digital_Write_HIGH(BYTE byteValue);

F81966_DLL_API void F81966_LPC_Set_WDT_Enable(BYTE byteValue);
F81966_DLL_API void F81966_LPC_Set_WDT_Disable();

```

## Digital Input / Output test

Note when using the following boards: 2I640SW

CIO1 needs to be controlled by CIO3

	Digital output Low Byte		Digital iutput Low Byte	
CIO1	Do	0	Di	0
	Do	1	Di	1
	Do	2	Di	2
	Do	3	Di	3
CIO2	Do	4	Di	4
	Do	5	Di	5
	Do	6	Di	6
	Do	7	Di	7

	Digital output High Byte		Digital iutput High Byte	
CIO3	Do	8	Di	8
	Do	9	Di	9
	Do	10	Di	10
	Do	11	Di	11
CIO4	Do	12	Di	12
	Do	13	Di	13
	Do	14	Di	14
	Do	15	Di	15

## sample code

```
Set CIO1 CIO2 Digital Output all high  
F81966_LPC_Digital_Write_LOW(256);
```

```
Set CIO1 CIO2 Digital Output all low  
F81966_LPC_Digital_Write_LOW(0);
```

```
Set CIO1 Digital Output bit 4 high  
F81966_LPC_Digital_Write_LOW(16);
```

```
Set CIO2 Digital Output bit 10 high  
F81966_LPC_Digital_Write_HIGH(4);
```

```
Read Din  
value = F81966_LPC_Digital_Read_LOW();
```

## Watch Dog test sample code

```
Set WDT 10 sec  
F81966_LPC_Set_WDT_Enable(10);
```

```
Disable WDT  
F81966_LPC_Set_WDT_Disable();
```

## 3-7-2 IO Device: F81966 LPC DIO under Linux console

### The Sample code source you can download from

[http://tprd.info/lexwiki/index.php/IO\\_Device:F81966\\_LPC\\_DIO\\_under\\_Linux\\_console](http://tprd.info/lexwiki/index.php/IO_Device:F81966_LPC_DIO_under_Linux_console)

Source file: F81966\_DIO\_v1.1L\_Src

Binary file: F81966\_DIO\_v1.1L\_Bin\_x86 F81966\_DIO\_v1.1L\_Bin\_x64

F81966 Library : F81966\_LIB\_v1.1L\_x86 F81966\_LIB\_v1.1L\_x64

## Introduction F81966 DIO

```
root@ubuntu: /home/test/Desktop/f81966/F81966_DIO
root@ubuntu: /home/test/Desktop/f81966/F81966_DIO# ./f81966 -h
F81966 OPEN FAIL!!!!
Usage: ./f81966 [OPTION] ... [--mode value]

-h,--help                printf this help and exit
-s DOx, --setDo value    | value:number of bits
-r DIX, --readD value    | value:number of bits
--CIO12                  | test CIO1,CIO2
--CIO34                  | test CIO3,CIO4
Example:
./f81966 --CIO12

root@ubuntu: /home/test/Desktop/f81966/F81966_DIO#
```

1. Start test Button, Send bits one by one and one by one receive
2. CIO116 mode for CIO116 module use
3. Set time countdown, If the end of the countdown, the trigger signal to reboot
4. Enable loop, Continuously reset the WDT to ensure that when the system is normal, the restart signal will not be triggered.

## F81966\_DLL Function

```
bool F81966_OPEN();

void F81966_Init();

void F81966_LPC_Write(BYTE LDNData, BYTE reg, BYTE value);

BYTE F81966_LPC_Read(BYTE LDNData, BYTE reg);

void F81966_LPC_Digital_Write_LOW(BYTE byteValue);

void F81966_LPC_Digital_Write_HIGH(BYTE byteValue);

BYTE F81966_LPC_Digital_Read_LOW();

BYTE F81966_LPC_Digital_Read_HIGH();

void F81966_LPC_Set_WDT_Enable(BYTE byteValue);

void F81966_LPC_Set_WDT_Disable();

void EntryLPC();

void ExitLPC();
```

## Digital Input / Output test

Note when using the following boards: 2I640SW  
CIO1 needs to be controlled by CIO3

	Digital output Low Byte		Digital iutput Low Byte	
CIO1	Do	0	Di	0
	Do	1	Di	1
	Do	2	Di	2
	Do	3	Di	3
CIO2	Do	4	Di	4
	Do	5	Di	5
	Do	6	Di	6
	Do	7	Di	7

	Digital output High Byte		Digital iutput High Byte	
CIO3	Do	8	Di	8
	Do	9	Di	9
	Do	10	Di	10
	Do	11	Di	11
CIO4	Do	12	Di	12
	Do	13	Di	13
	Do	14	Di	14
	Do	15	Di	15

## sample code

```
Set CIO1 CIO2 Digital Output all high  
F81966_LPC_Digital_Write_LOW(256);
```

```
Set CIO1 CIO2 Digital Output all low  
F81966_LPC_Digital_Write_LOW(0);
```

```
Set CIO1 Digital Output bit 4 high  
F81966_LPC_Digital_Write_LOW(16);
```

```
Set CIO2 Digital Output bit 10 high  
F81966_LPC_Digital_Write_HIGH(4);
```

```
Read Din  
value = F81966_LPC_Digital_Read_LOW();
```

## Watch Dog test

### sample code

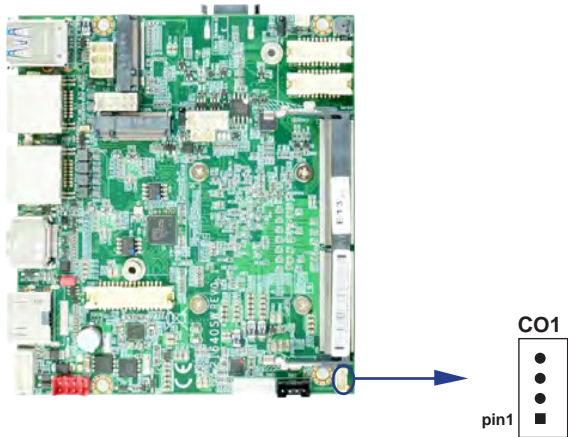
```
Set WDT 10 sec  
F81966_LPC_Set_WDT_Enable(10);
```

```
Disable WDT  
F81966_LPC_Set_WDT_Disable();
```



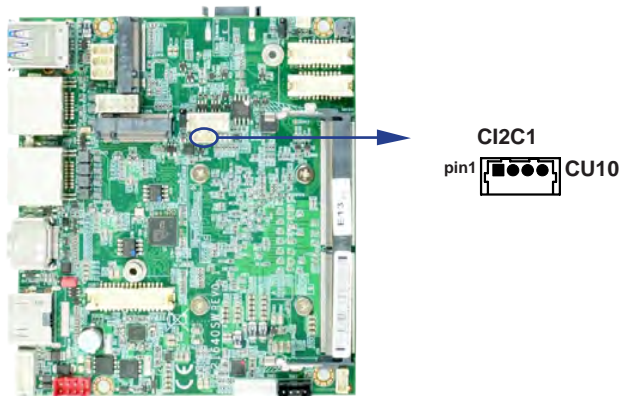
**3-8 CO1: SMBus 1x4 pin (1.25mm) wafer**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+3.3V	2	GND
3	SMBus-Clock	4	SMBus-Data



**3-9 CI2C1: I2C Bus 1x4 pin (1.25mm) wafer**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+3.3V	2	GND
3	I2C Clock	4	I2C Data



**3-10 CPI1: DC Power input 1x4 pin (2.0mm) wafer (RED)**

PIN NO.	DESCRIPTION
1,2,7,8	GND
3,4,5,6	DC-IN

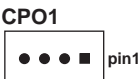
Note: Very important check DC-in Voltage.



**3-11 CPO1: +12V / +5V DC voltage output 1x4 pin (2.0mm) wafer (Black)**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+5V	2	GND
3	GND	4	+12V

Note: Attention! Check Device Power in spec.

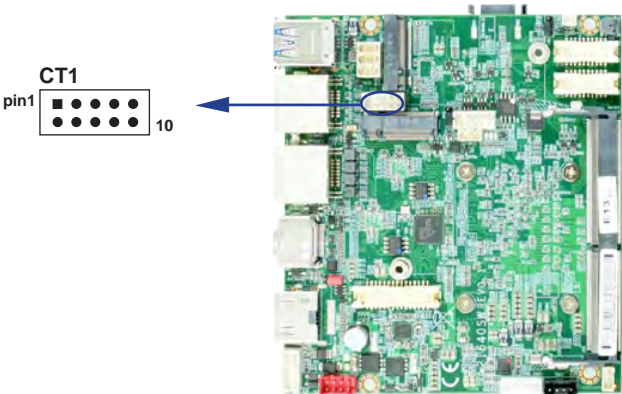


### 3-12 Touch screen 2x5 pin (2.0mm) USB interface

• For 8-wire type pin define

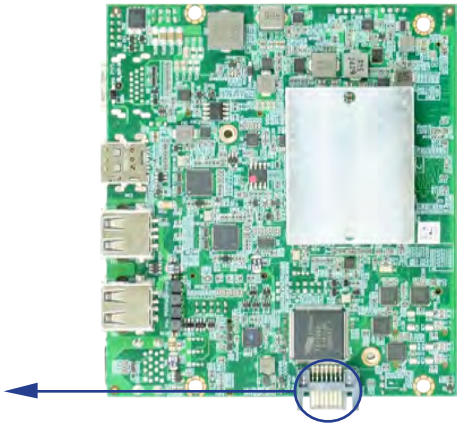
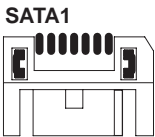
PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	Bottom	2	Bottom Sense
3	Top Sense	4	Top
5	Right	6	Right Sense
7	Left	8	Left Sense
9	GND	10	NC

Note: For eight wire type cable Pin 3 and Pin4 need short.



### 3-13 SATA1: SATA port 1x7pin Connector

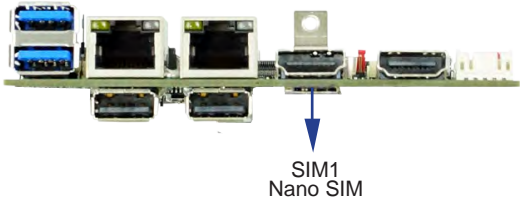
PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	GND	2	TX+
3	TX-	4	GND
5	RX-	6	RX+
7	GND		



### 3-14 SIM1: Nano SIM Card Push-Push

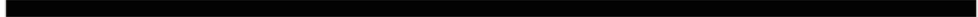
• Follow ISO 7816-2 Smart Card Standard.

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	VCC	2	RST
3	CLK	4	NC
5	GND	6	VPP
7	DATA	8	NC



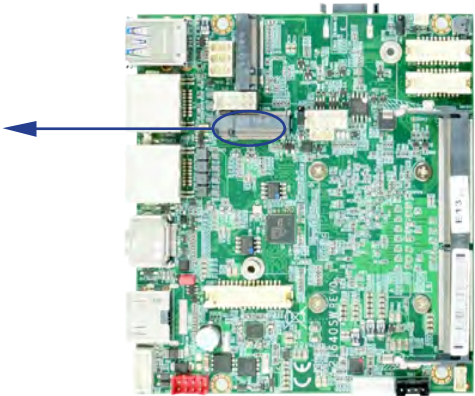
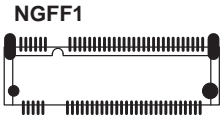
### 3-15 NGFF1: PCI Express M.2 B key 2242 / 3042 H=8.5 sockets 75pin

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	CFG3_USB3_PCIE	2	+3.3V / +3.7V
3	GND	4	+3.3V / +3.7V
5	GND	6	FULL_CARD_PWR
7	USB2.0_P	8	W_DISABLE_1
9	USB2.0_N	10	M2_LED
11	NC		
B Key notch			
		20	NC
21	GND	22	NC
23	NC	24	NC
25	NC	26	W_DISABLE_2
27	GND	28	NC
29	USB3Rn \ PERn1	30	SIM_RST_M2
31	USB3Rp \ PERp1	32	SIM_CLK_M2
33	GND	34	SIM_DATA_M2
35	USB3Tn \ PETn1	36	SIM_PWR_M2
37	USB3Tp \ PETp1	38	NC
39	GND	40	NC
41	SATA-RX+ \ PERn0	42	NC
43	SATA-RX- \ PERp0	44	NC
45	GND	46	NC
47	SATA-TX- \ PETn0	48	NC
49	SATA-TX+ \ PETp0	50	PREST
51	GND	52	SRCCCLKREQ_N
53	PCIE_CLK_N0	54	NC
55	PCIE_CLK_P0	56	NC
57	GND	58	NC
59	NC	60	NC



PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
61	NC	62	NC
63	NC	64	NC
65	NC	66	SIM_DET
67	MD_RESET_N	68	NC
69	NC	70	+3.3V / +3.7V
71	GND	72	+3.3V / +3.7V
73	GND	74	+3.3V / +3.7V
75	CONFIG_2		

- Note:
- 1. NGFF1 support USB3.0 / SATA-SSD .PCIe x2 by BOM control.
  - 2. VCC voltage default support +3.3V.
  - 3. BOM control, if need 4G LTE device VCC voltage is +3.7V.

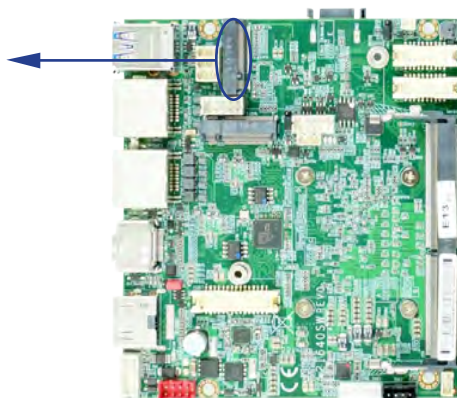
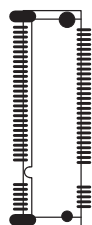


### 3-16 NGFF2: PCI Express M.2 B key 2242/3042 H=8.5 sockets 75pin

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	GND	2	+3.3V
3	GND	4	+3.3V
5	GND	6	FULL_CARD_PWR
7	USB2.0_P	8	W_DISABLE_1
9	USB2.0_N	10	M2_LED
11	NC		
B Key notch			
		20	NC
21	GND	22	NC
23	NC	24	NC
25	NC	26	W_DISABLE_2
27	GND	28	NC
29	NC	30	NC
31	NC	32	NC
33	GND	34	NC
35	NC	36	NC
37	NC	38	NC
39	GND	40	NC
41	SATA-RX+ \ PERn0	42	NC
43	SATA-RX- \ PERp0	44	NC
45	GND	46	NC
47	SATA-TX- \ PETn0	48	NC
49	SATA-TX+ \ PETp0	50	PREST
51	GND	52	SRCCCLKREQ_N
53	PCIE_CLK_N0	54	NC
55	PCIE_CLK_P0	56	NC
57	GND	58	NC
59	NC	60	NC

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
61	NC	62	NC
63	NC	64	NC
65	NC	66	SIM_DET
67	NC	68	NC
69	CFG1_O-PCIE_G-SATA	70	+3.3V
71	GND	72	+3.3V
73	GND	74	+3.3V
75	GND		

NGFF2

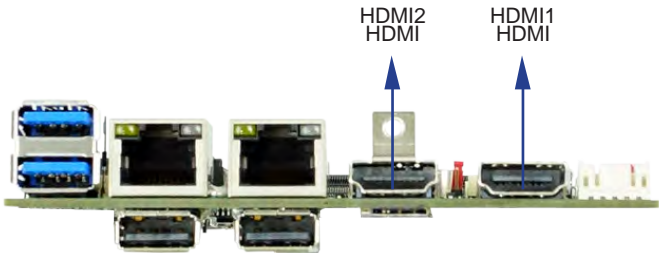




### 3-17 Display Interface

• HDMI1 / HDMI2: HDMI type A connector

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	TMDS DATA2+	2	GND
3	TMDS DATA2-	4	TMDS DATA1+
5	GND	6	TMDS DATA1-
7	TMDS DATA0+	8	GND
9	TMDS DATA0-	10	TMDS CLK+
11	GND	12	TMDS CLK-
13	NC	14	NC
15	DDC CLOCK	16	DDC DATA
17	GND	18	+5V
19	H.P. Detect		



• **DP1: DisplayPort connector (option colay HDMI1)**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	Lane0+	2	GND
3	Lane0-	4	Lane1+
5	GND	6	Lane1-
7	Lane2+	8	GND
9	Lane2-	10	Lane3+
11	GND	12	Lane3-
13	GND	14	GND
15	AUX_CH+	16	GND
17	AUX_CH-	18	H.P. Detect
19	GND	20	+5V

### 3-18 LVDS1 : LVDS interface 2x15 pin (1.25mm) wafer

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	PWM dimming	2	+5V
3	+LCD(5V or 3.3V)	4	+LCD(5V or 3.3V)
5	Channel-1-DATA3+	6	Channel-0-DATA3+
7	Channel-1-DATA3-	8	Channel-0-DATA3-
9	Channel-0-DATA2+	10	Channel-0-CLK+
11	Channel-0-DATA2-	12	Channel-0-CLK-
13	GND	14	GND
15	Channel-0-DATA1+	16	Channel-0-DATA0+
17	Channel-0-DATA1-	18	Channel-0-DATA0-
19	PANL_HPD_N	20	GND
21	+LCD(5V or 3.3V)	22	+LCD(5V or 3.3V)
23	Channel-1-DATA2+	24	Channel-1-CLK+
25	Channel-1-DATA2-	26	Channel-1-CLK-
27	Channel-1-DATA1+	28	Channel-1-DATA0+
29	Channel-1-DATA1-	30	Channel-1-DATA0-

Note:

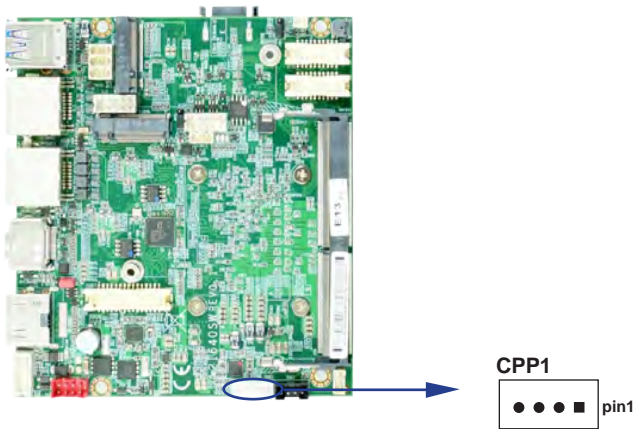
1. LVDS interface supports 18/24bits two channel.
2. JVL1: LVDS panel +5V/+3.3V (default) Voltage select.
3. LVDS1 PIN 1 for panel backlight dimming control.
4. PIN 19 HPD is active "L", recommend connect to GND.



**3-19 CPP1: LVDS Panel backlight power 1x5 pin (2.0mm) wafer**

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+12V	2	GND
3	PWM dimming	4	ENBKL (3.3V)
5	ENBKL ( 5V )		

Note: CPP1 PIN 3 and LVDS1 PIN1 is same signal.



### 3-20 LVDS1 : eDP interface 2x15 pin (1.25mm) wafer

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	NC	2	NC
3	+LCD(5V or 3.3V)	4	+LCD(5V or 3.3V)
5	NC	6	NC
7	NC	8	NC
9	eDP_TX0P	10	eDP_AUXP
11	eDP_TX0N	12	eDP_AUXN
13	GND	14	GND
15	eDP_TX1P	16	eDP_HPD
17	eDP_TX1N	18	NC
19	PANL_HPD_N	20	GND
21	+LCD(5V or 3.3V)	22	+LCD(5V or 3.3V)
23	NC	24	NC
25	NC	26	NC
27	NC	28	NC
29	NC	30	NC

Note:

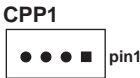
1. JVL1: eDP panel +5V/+3.3V (default) Voltage select.
2. PIN 19 HPD is active "L", recommend connect to GND.



### 3-21 CPP1: eDP Panel backlight power 1x5 pin (2.0mm) wafer

PIN NO.	DESCRIPTION	PIN NO.	DESCRIPTION
1	+12V	2	GND
3	PWM dimming	4	ENBKL (3.3V)
5	ENBKL ( 5V )		

Note: CPP1 PIN 3 and LVDS1 PIN1 is same signal.



### 3-22 Connector wafer of Compatible Brand and part number list

Location	CKTS	PITCH	Brand Name	Mating connector	Cable housing
CFP1	2x5 10Pin	2.00mm	JST	B10B-PHDSS	PHDR-10VS
CIO1	2x5 10Pin	2.00mm	JST	B10B-PHDSS	PHDR-10VS
CO1	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CI2C1	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CPI1	2x4 8Pin	2.00mm	JST	B8B-PHDSS	PHDR-08VS
CPO1	1x4 4Pin	2.00mm	JST	B4B-PH-KL	PHR-4
CU8	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CU9	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
CU10	1x4 4Pin	1.25mm	MOLEX	53047-0410	51021-0400
SWP1	1x2 2Pin	2.00mm	JST	B2B-PH-KL	PHR-2

---

# Chapter-4

## Introduction of BIOS

The BIOS is a program located in the Flash Memory on the motherboard.

This program is a bridge between motherboard and operating system.

When you start the computer, the BIOS program gains control.

The BIOS first operates an auto-diagnostic test called POST (Power on Self Test) for all the necessary hardware, it detects the entire hardware devices and configures the parameters of the hardware synchronization. After these tasks are completed, BIOS will give control of the computer back to operating system (OS). Since the BIOS is the only channel for hardware and software to communicate with, it is the key factor of system stability and of ensuring your system performance at best.

In the BIOS Setup main menu, you can see several options. We will explain these options in the following pages. First, let us see the function keys you may use here:

Press <Esc> to quit the BIOS Setup.

Press ↑↓←→ (up, down, left, right) to choose the option you want to confirm or modify.

Press <F10> to save these parameters and to exit the BIOS Setup menu after you complete the setup of BIOS parameters.

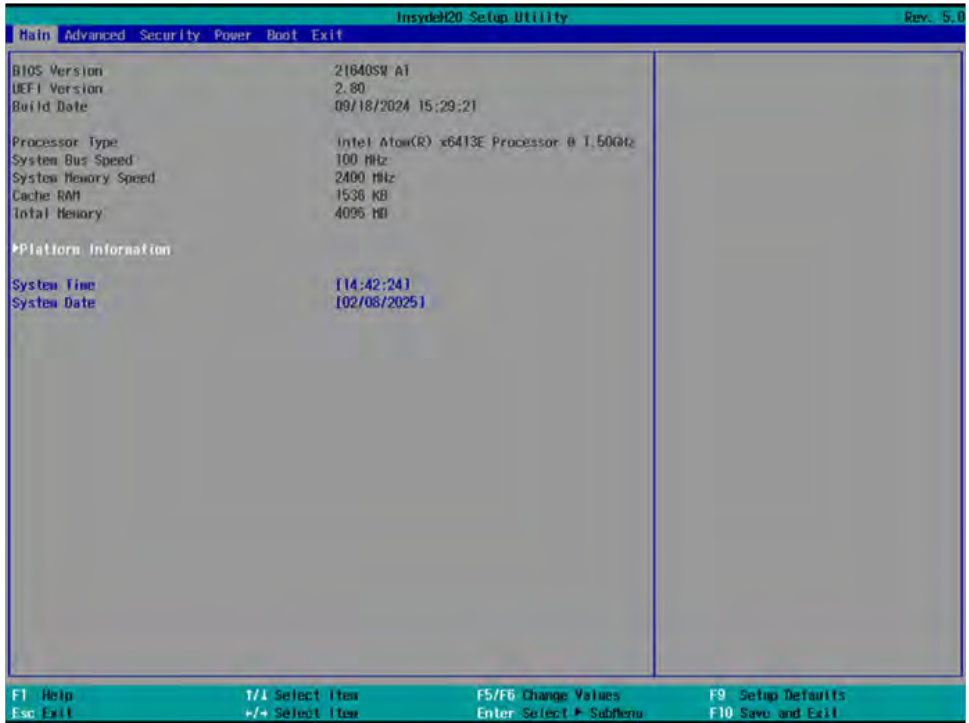
Press Page Up/Page Down or +/- keys to modify the BIOS parameters for the active option.

### 4-1 Enter Setup

Power on the computer and press <Del> key immediately to enter Setup.

If the message disappears before your respond but you still wish to enter Setup, restart the system by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart the system by simultaneously pressing <Ctrl>, <Alt> and <Delete> keys.

## 4-2 BIOS Menu Screen & Function Keys



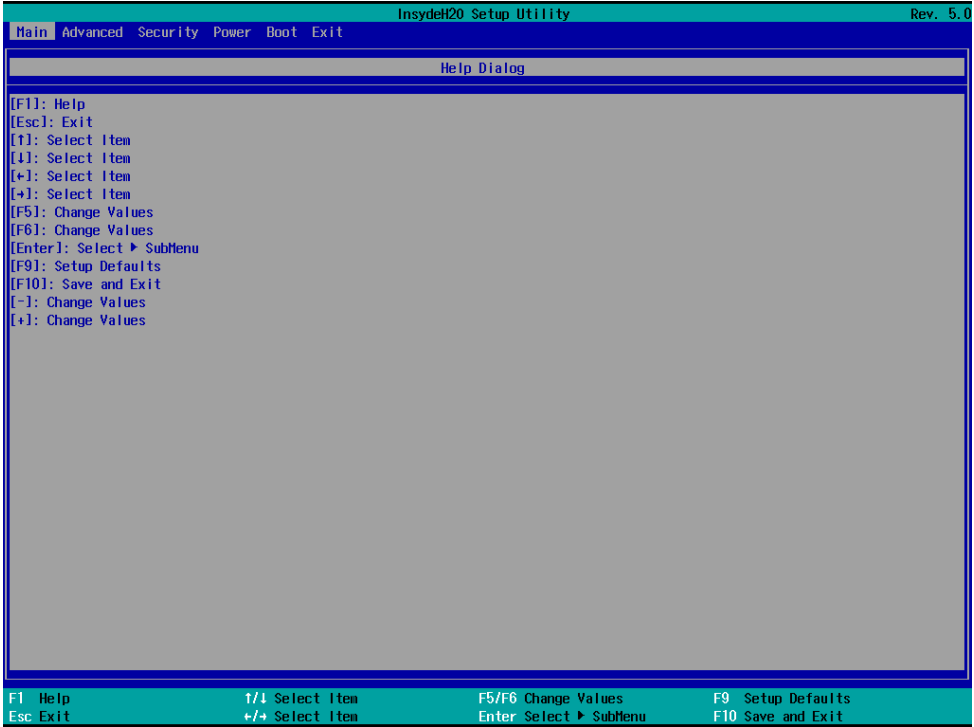
In the above BIOS Setup main menu of, you can see several options.

We will explain these options step by step in the following pages of this chapter, but let us first see a short description of the function keys you may use here:

- Press >< (right, left) to select screen;
- Press ↑↓ (up, down) to choose, in the main menu, the option you want to confirm or to modify.
- Press <Enter> to select.
- Press <+>/<-> or <F5>/<F6> keys when you want to modify the BIOS parameters for the active option.
- [F1]: General help.
- [F2]: Previous values.
- [F3]: Optimized defaults.
- [F4]: Save & Reset.
- Press <Esc> to quit the BIOS Setup.



### 4-3 Getting Help



#### Status Page Setup Menu / Option Page Setup Menu

Press F1 to pop up a help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press <Esc>.

### 4-4 Menu Bars

There are six menu bars on top of BIOS screen:

Main To change system basic configuration

Advanced To change system advanced configuration

Chipset To change PCH IO configuration

Security Password settings

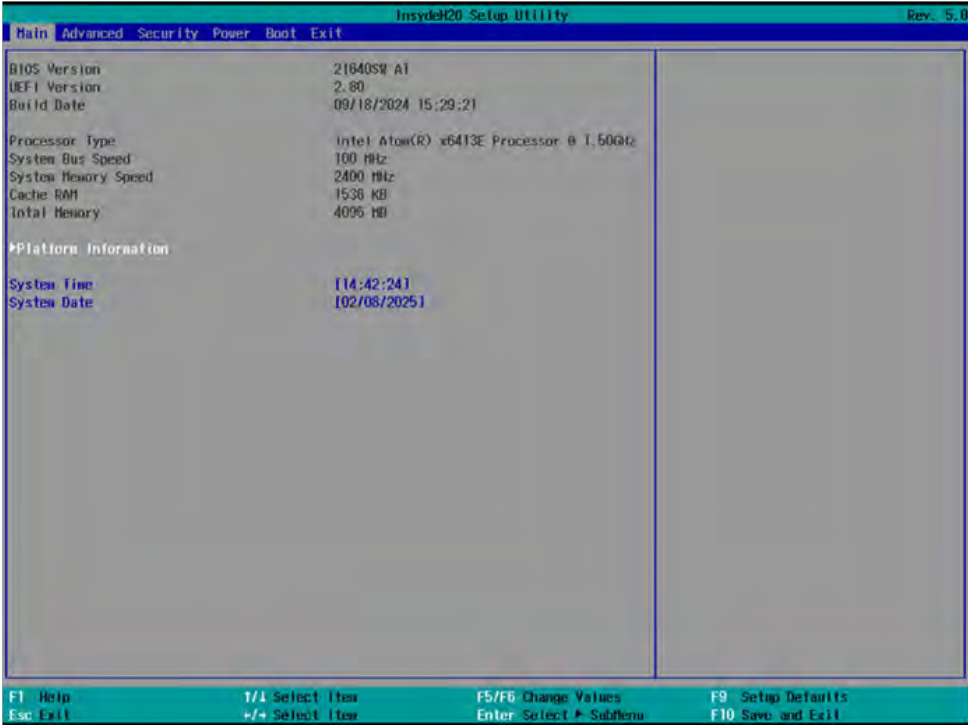
Boot Quiet boot or boot from USB selected.

Save & Exit Save setting, loading and exit options.

User can press the right or left arrow key on the keyboard to switch from menu bar.

The selected one is highlighted.

## 4-5 Main



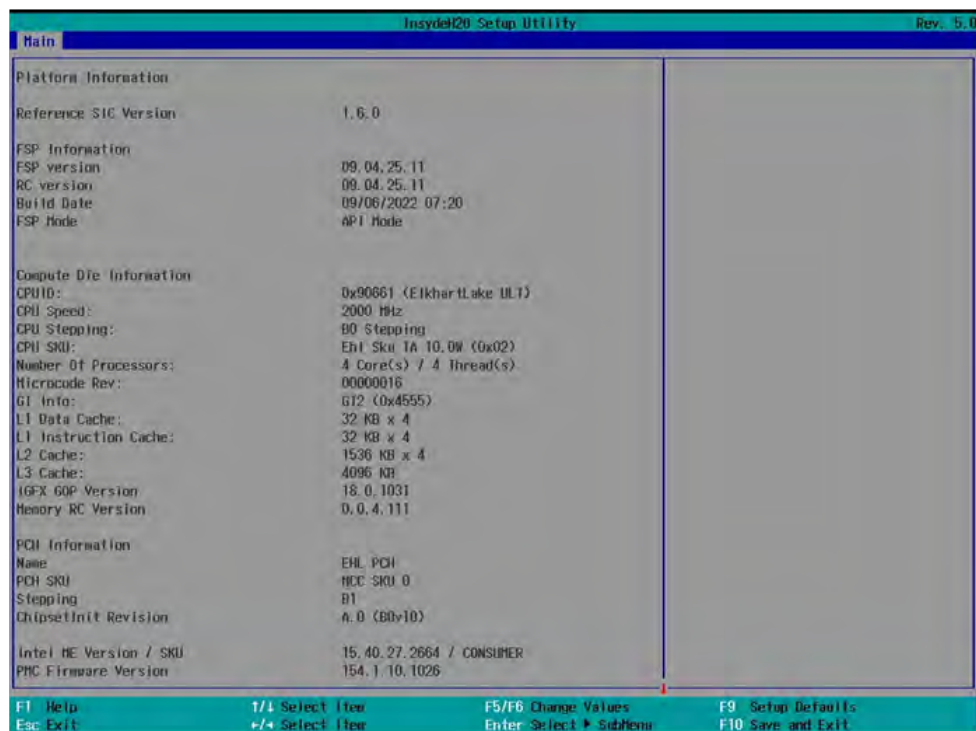
Main menu screen includes some basic system information. Highlight the item and then use the <+> or <-> and numerical keyboard keys to select the value you want in each item.

### System Date

Set the Date. Please use [Tab] to switch between data elements.

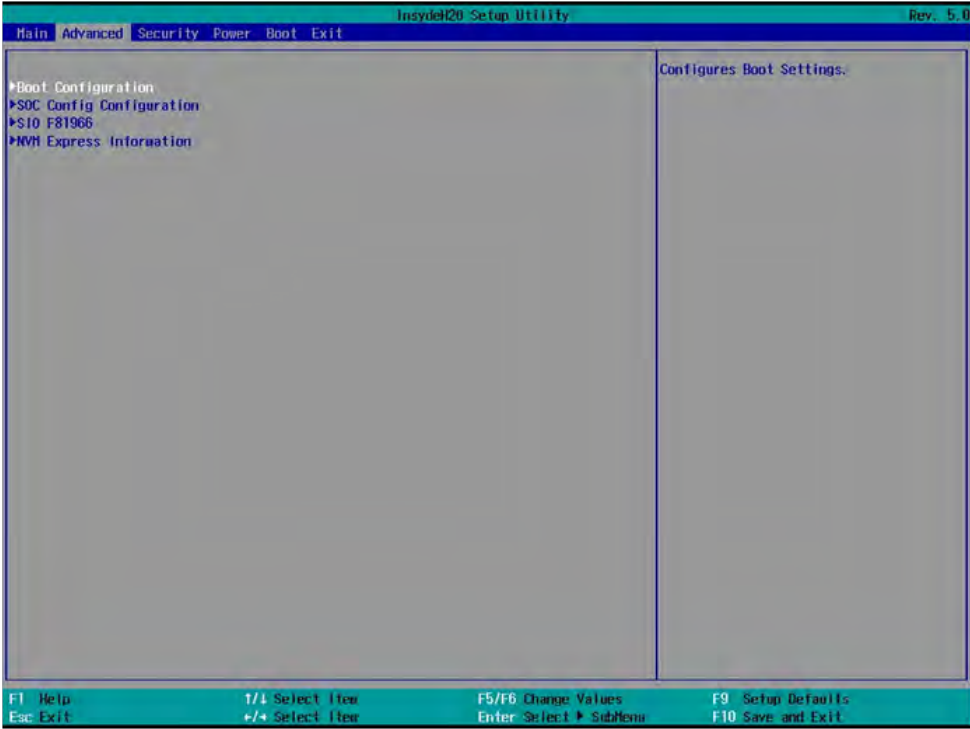
### System Time

Set the Time. Please use [Tab] to switch between data elements.



## Platform information

# 4-6 Advanced



## Boot Configuration

Please refer section 4-6-1

## SOC Config Configuration

Please refer section 4-6-2

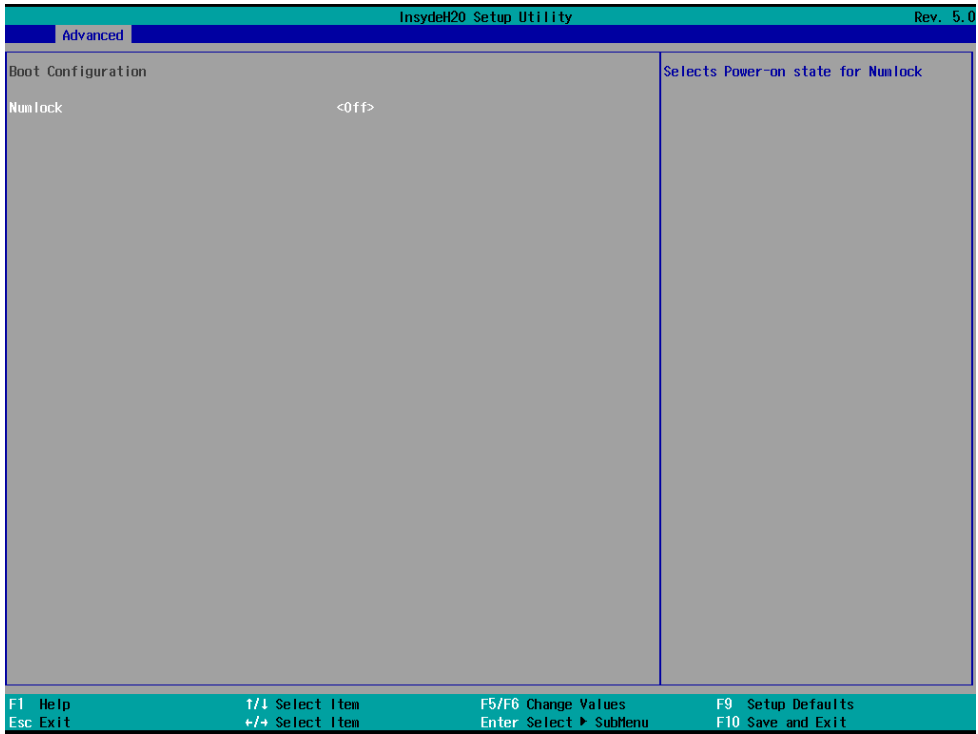
## SIO F81966

Please refer section 4-6-3

## NVM Express information

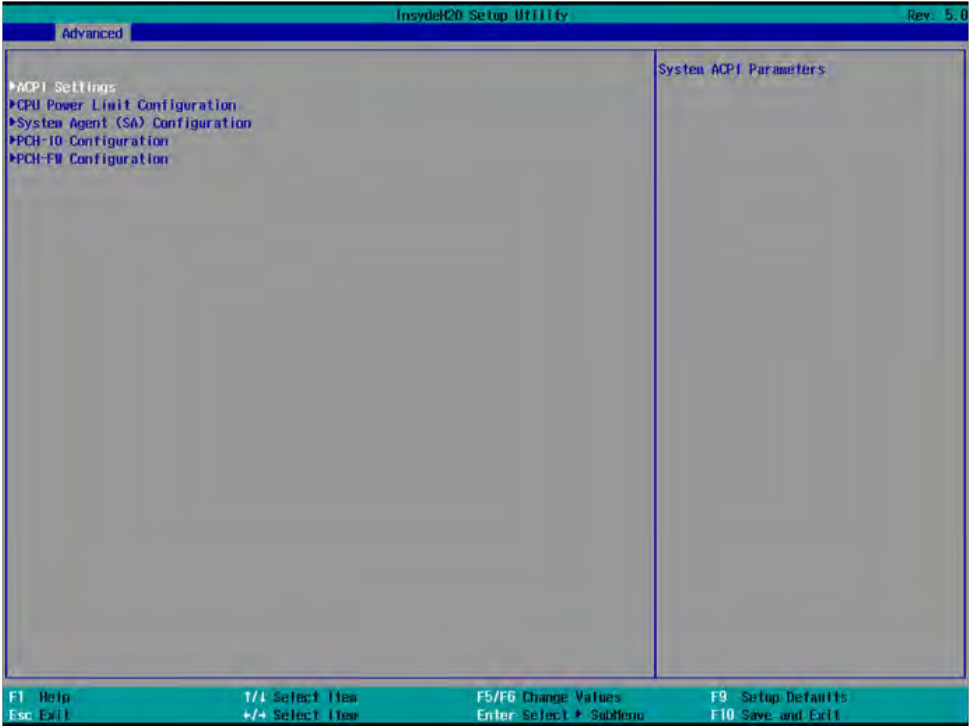
Please refer section 4-6-4

## 4-6-1 Boot Configuration



To select Power-on state for NumLock, default is <off>

## 4-6-2 SOC Config Configuration



### ACPI Settings

Please refer section 4-6-2-1

### CPU Power Limit Configuration

Please refer section 4-6-2-2

### System Agent (SA) Configuration

Please refer section 4-6-2-3

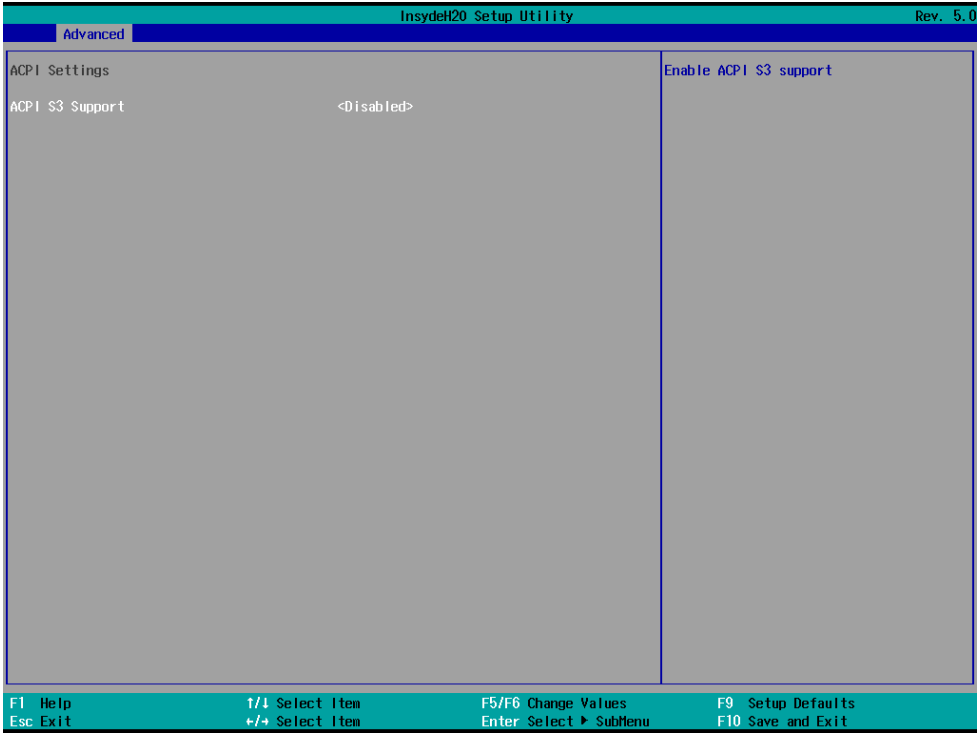
### PCH-IO Configuration

Please refer section 4-6-2-4

### PCH-FW Configuration

Please refer section 4-6-2-5

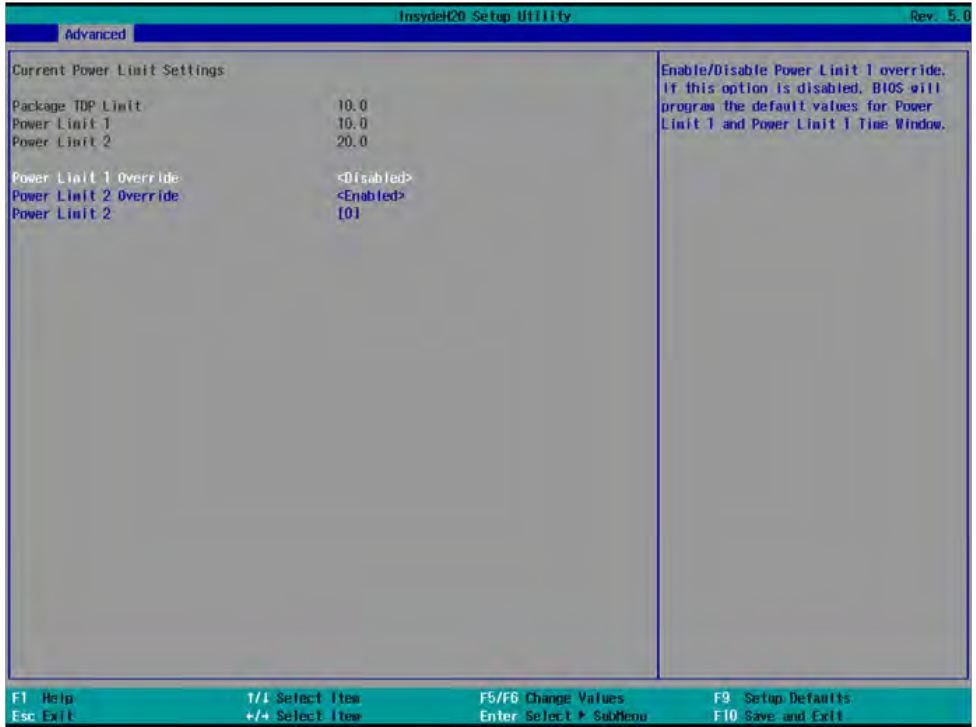
## 4-6-2-1 ► ACPI Settings



### ACPI S3 Support

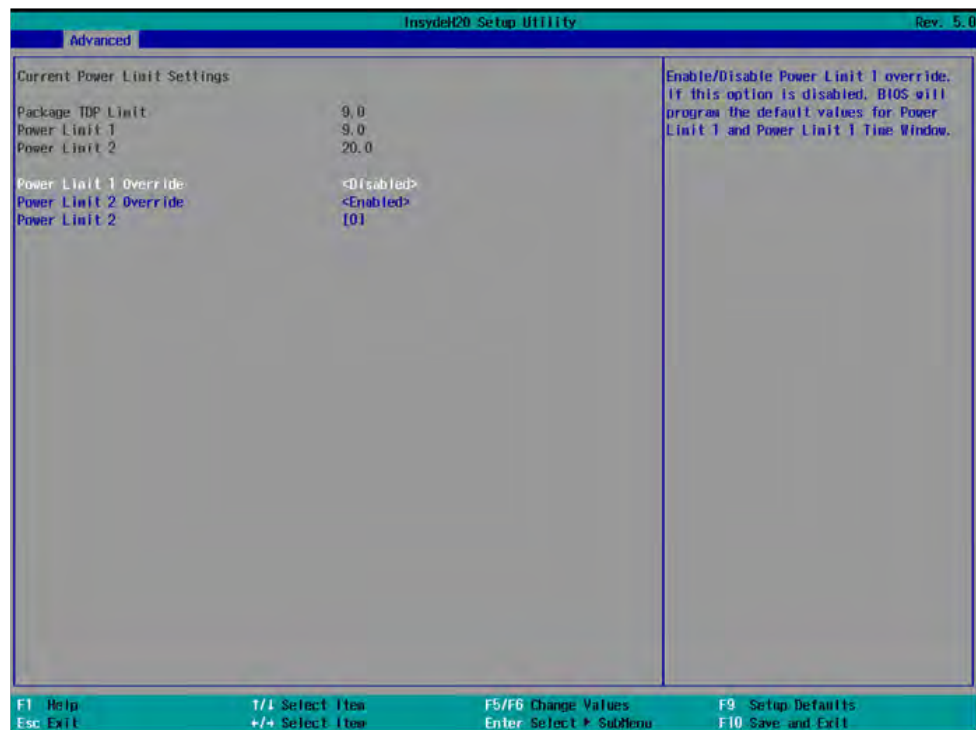
To enable BIOS support security device or not, default is Enabled.

## 4-6-2-2 ► CPU Power Limit Configuration



The setting follows INTEL Celeron J6412 CPU power limit default configuration.





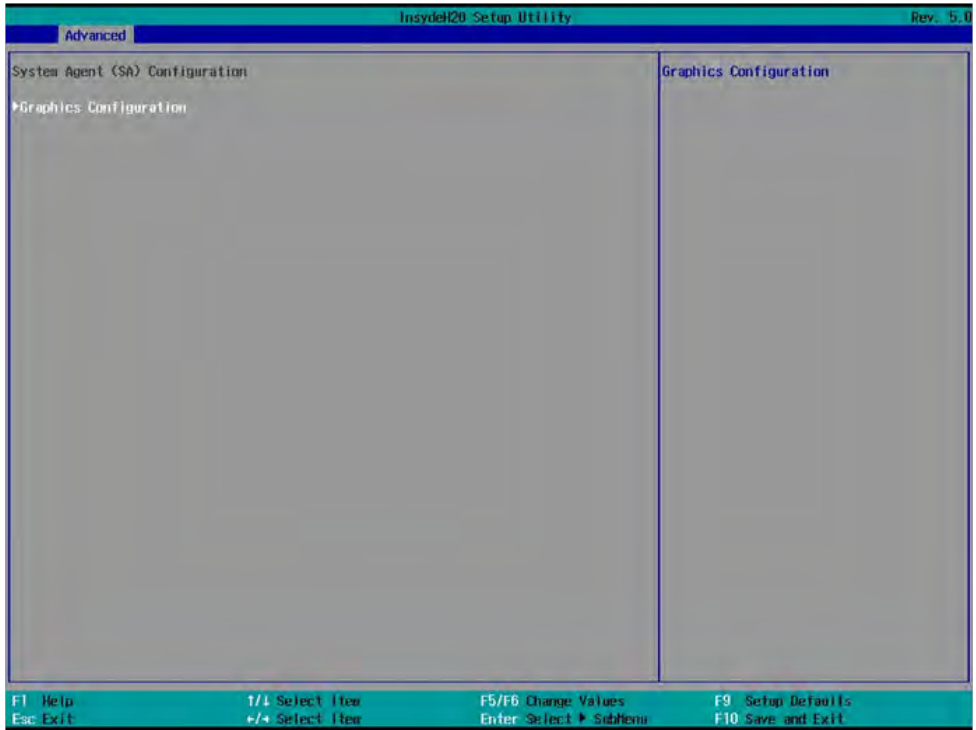
The setting follows INTEL Atom x6413E power limit default configuration.

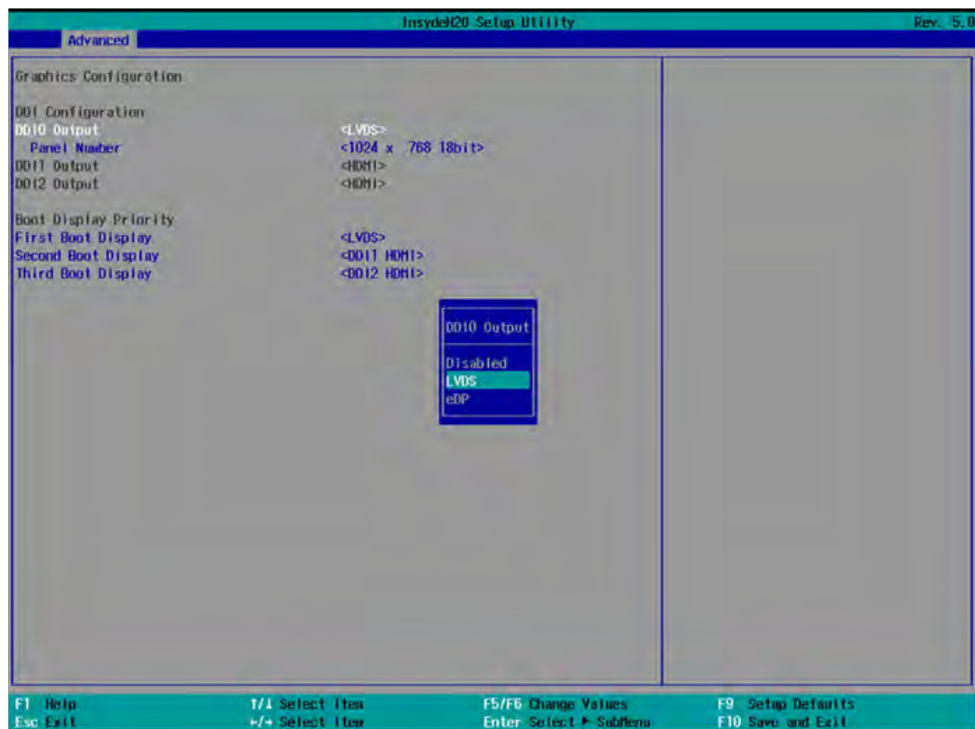


## Power Limit Override

Enable / disable PL1 / PL2 and enter the power numerical value from 0 to 20000 to get higher or lower CPU TDP

### 4-6-2-3 ► System Agent (SA) Configuration





## Graphic Configuration

To select First Boot Display priority, there are LVDS/eDP, DDI1 HDMI, DDI2 HDMI, default is LVDS

## First Boot Display

To select First Boot Display priority, there are LVDS/eDP, DDI1 HDMI, DDI2 HDMI, default is LVDS

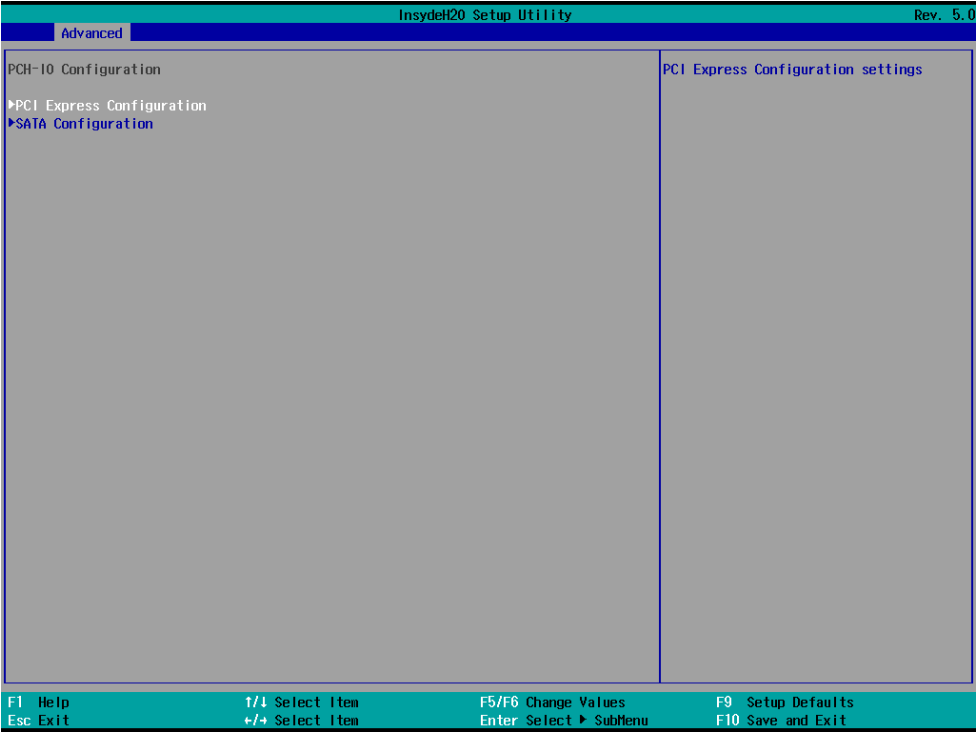
## Second Boot Display

To select Second Boot Display priority, there are DDI1 HDMI, DDI2 HDMI, default is DDI1 HDMI

## Third Boot Display

To select First Boot Display priority, there is DDI2 HDMI

## 4-6-2-4 ► PCH-IO Configuration



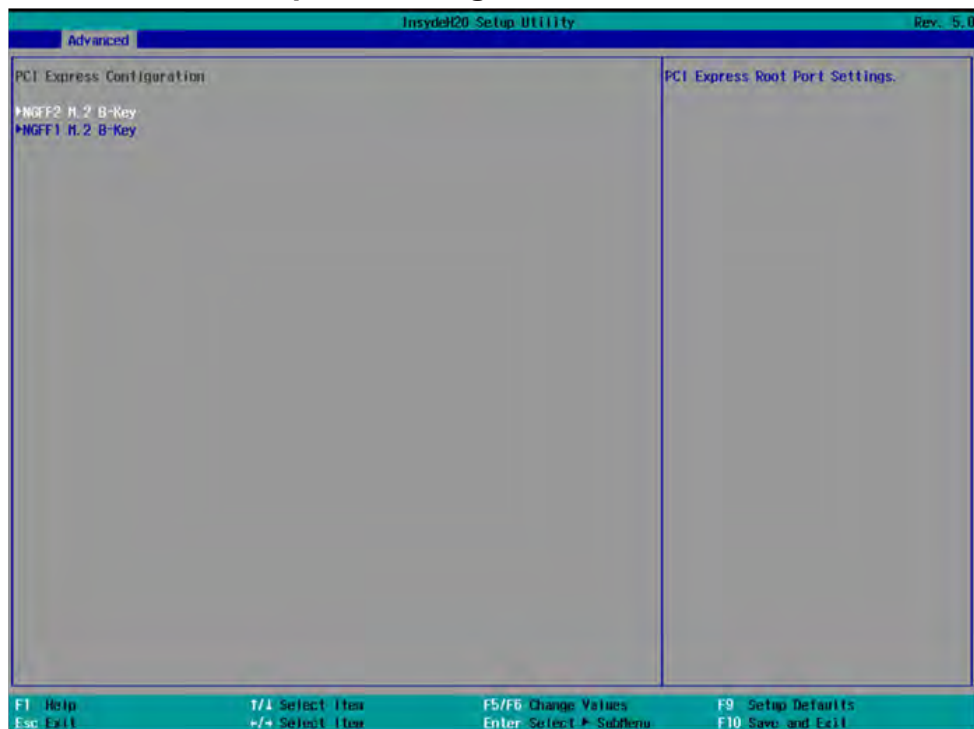
### PCI Express Configuration

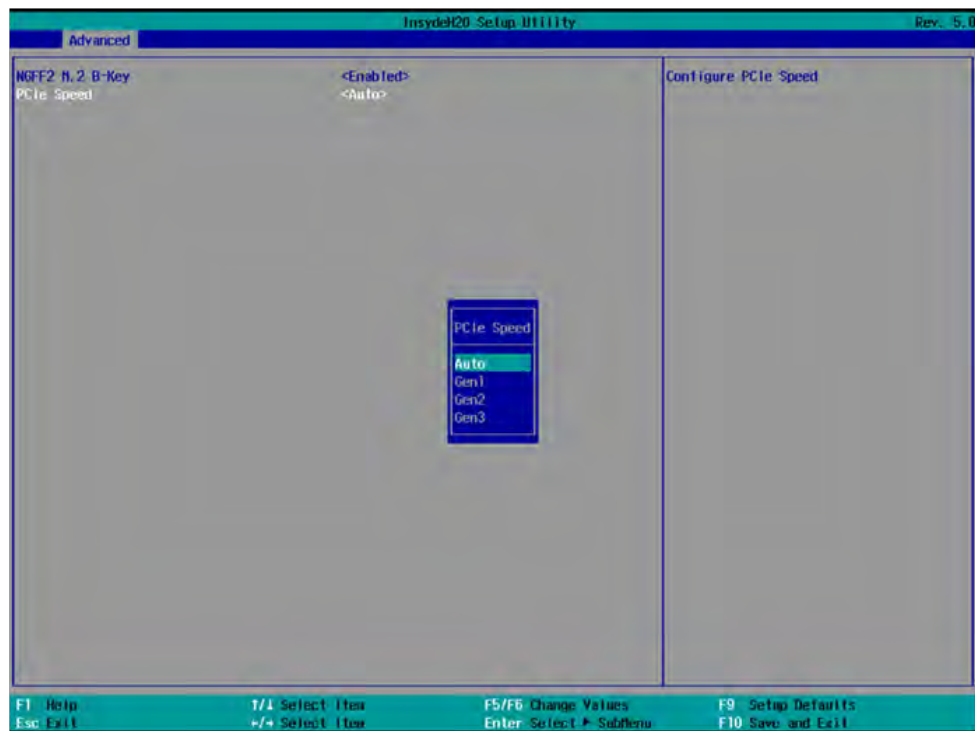
Please refer section 4-6-2-4-1

### SATA Configuration

Please refer section 4-6-2-4-2

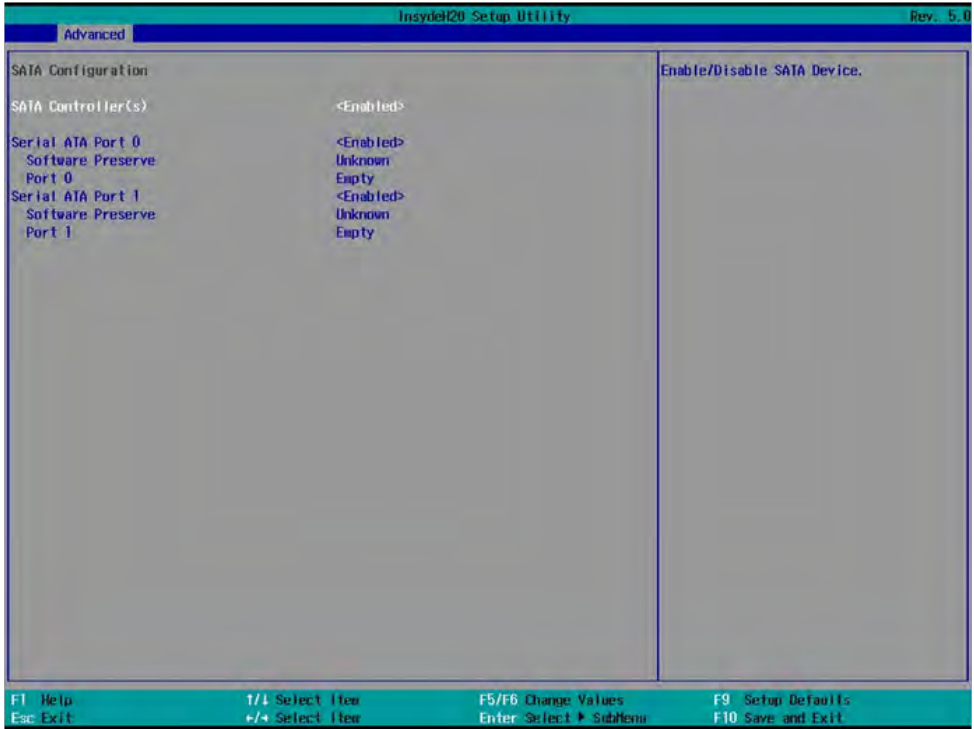
## 4-6-2-4-1 ► PCI Express Configuration





To select NGFF2 & NGFF1 device enabled or not and to change the PCIe Speed, there are Auto, Gen1, Gen2, Gen3, default is Auto

## 4-6-2-4-2 ▶ SATA Configuration



To select SATA1 & NGFF1 M.2 SATA device enabled or not.



4-6-2-5 ► PCH-FW Configuration

Advanced

InsydeH20 Setup Utility

Rev. 5.0

ME Firmware Version15.40.10.2252

ME Firmware ModeNormal Mode

ME Firmware SKUConsumer SKU

ME Firmware Status 10x90000255

ME Firmware Status 20x32850106

ME Firmware Version

F1 Help

Esc Exit

T/↓ Select Item

↑/↵ Select Item

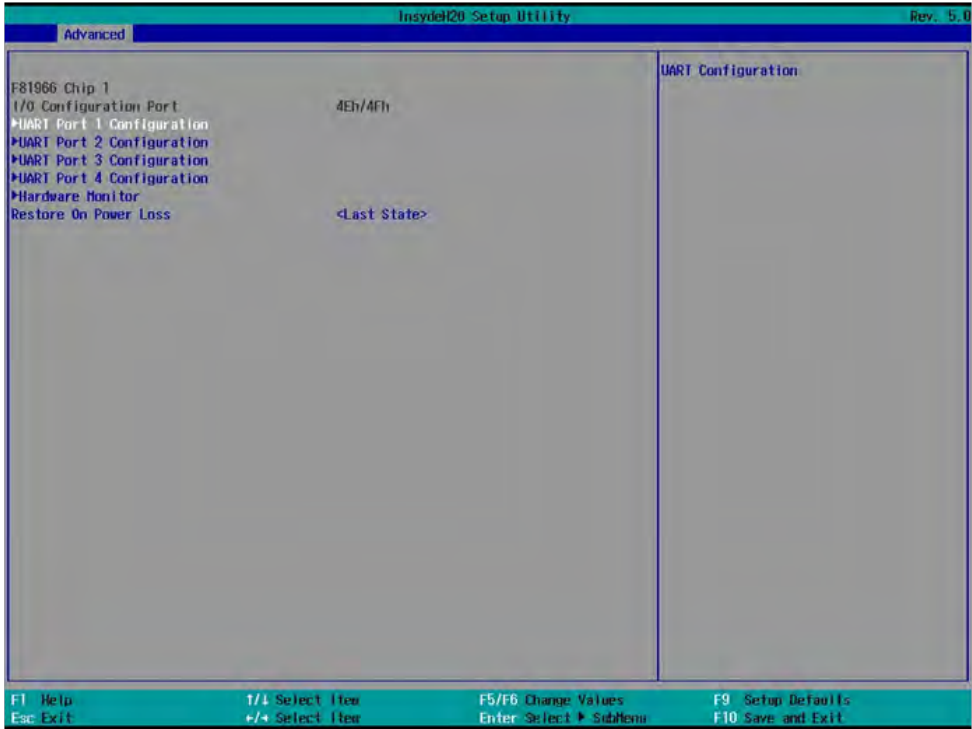
F5/F6 Change Values

Enter Select ► SubMenu

F9 Setup Defaults

F10 Save and Exit

### 4-6-3 SIO F81966



**UART Port 1 Configuration**

Please refer section 4-6-3-1

**UART Port 2 Configuration**

Please refer section 4-6-3-2

**UART Port 3 Configuration**

Please refer section 4-6-3-3

**UART Port 4 Configuration**

Please refer section 4-6-3-4

**Hardware Monitor**

Please refer section 4-6-3-5

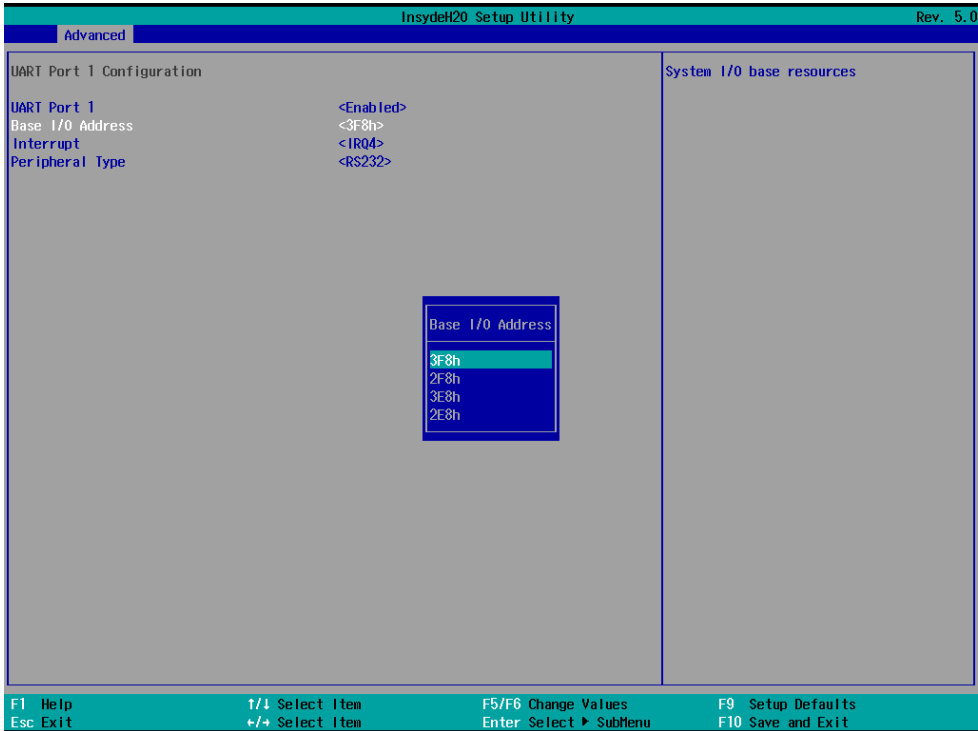
**Restore on Power Loss**

Please refer section 4-6-3-4

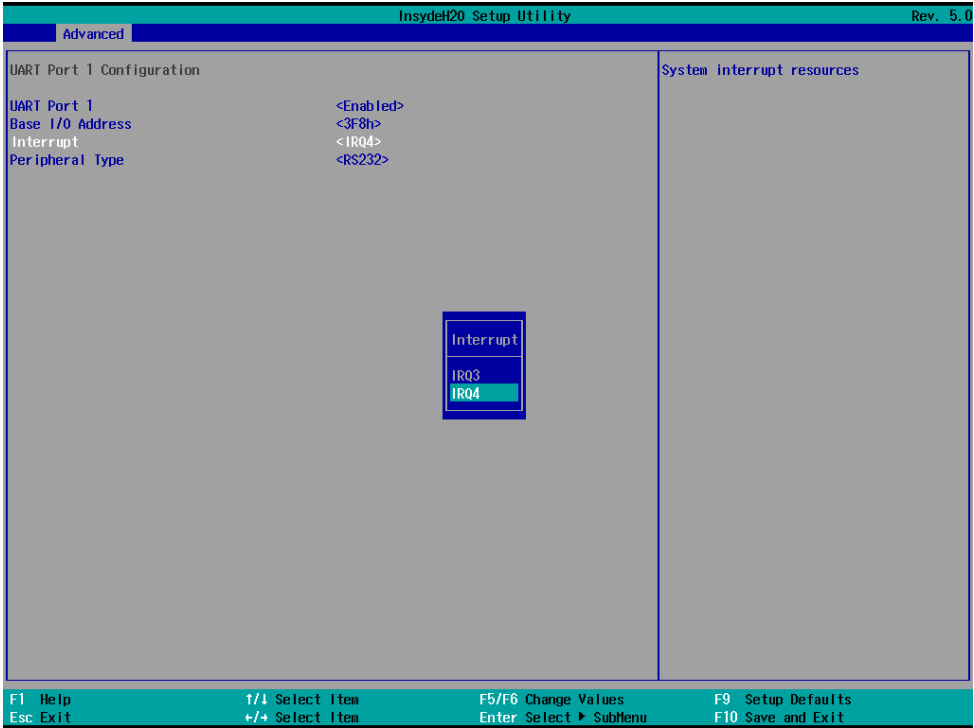
# 4-6-3-1 ► UART Port 1 Configuration

InsydeH20 Setup Utility		Rev. 5.0
Advanced		
UART Port 1 Configuration		Configure UART Port using options : [Disabled] Disable device [Enabled] Enable device and use below settings
UART Port 1	<Enabled>	
Base I/O Address	<3F8h>	
Interrupt	<IRQ4>	
Peripheral Type	<RS232>	
<div>UART Port 1 Disabled Enabled</div>		
F1 Help	T/↓ Select Item	F5/F6 Change Values
Esc Exit	←/→ Select Item	Enter Select ► SubMenu
		F9 Setup Defaults
		F10 Save and Exit

To Enable Serial port or not, default is Enabled.



Base I/O Address, default is 3F8h.

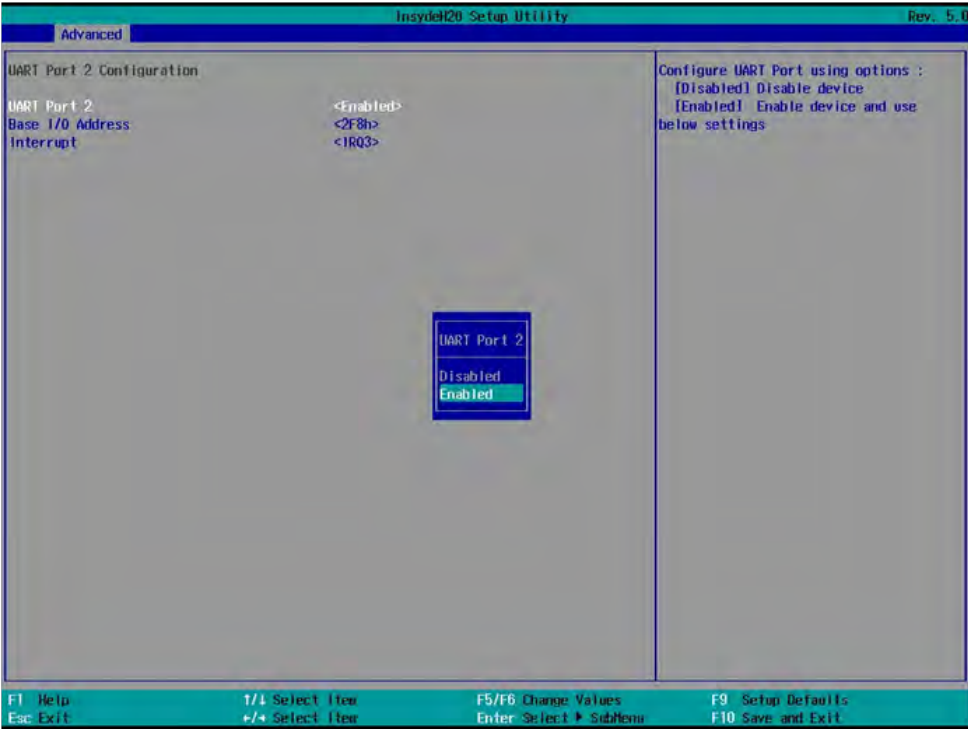


Interrupt, default is IRQ4.

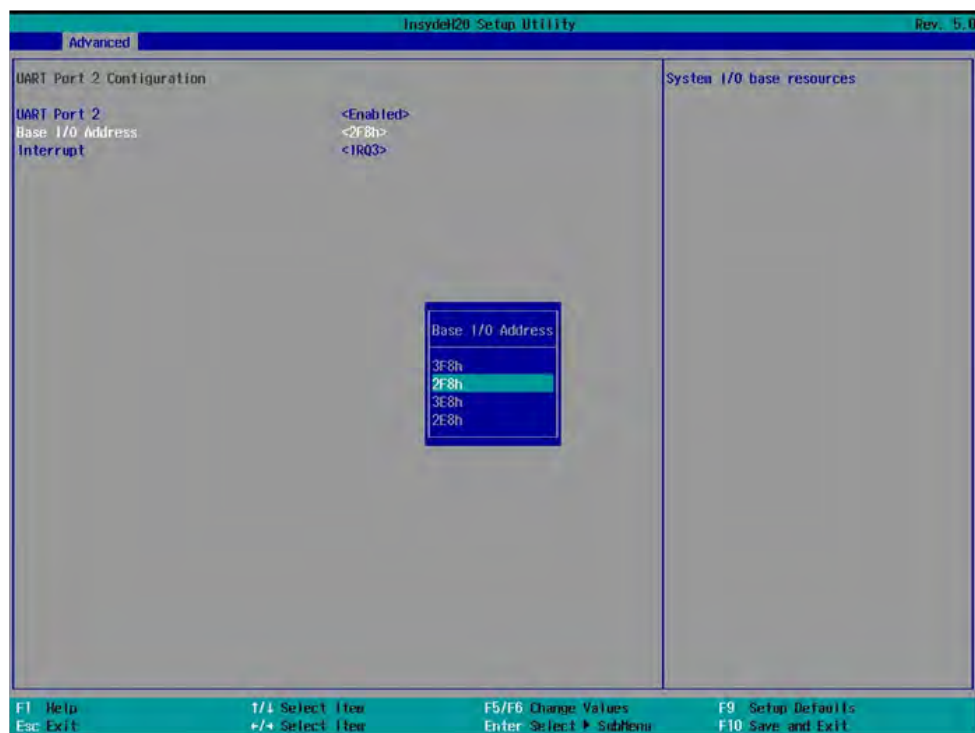


Peripheral, to select the Serial port to RS232 / RS422 / RS485, default is RS232.

4-6-3-2 ► UART Port 2 Configuration

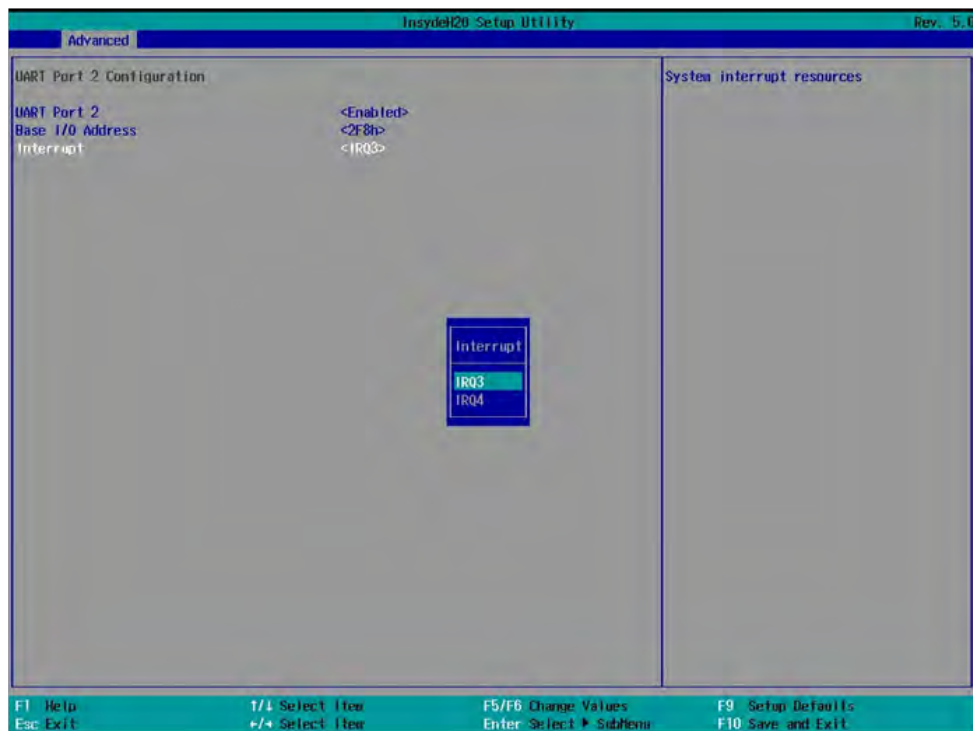


To Enable Serial port or not, default is Enabled.



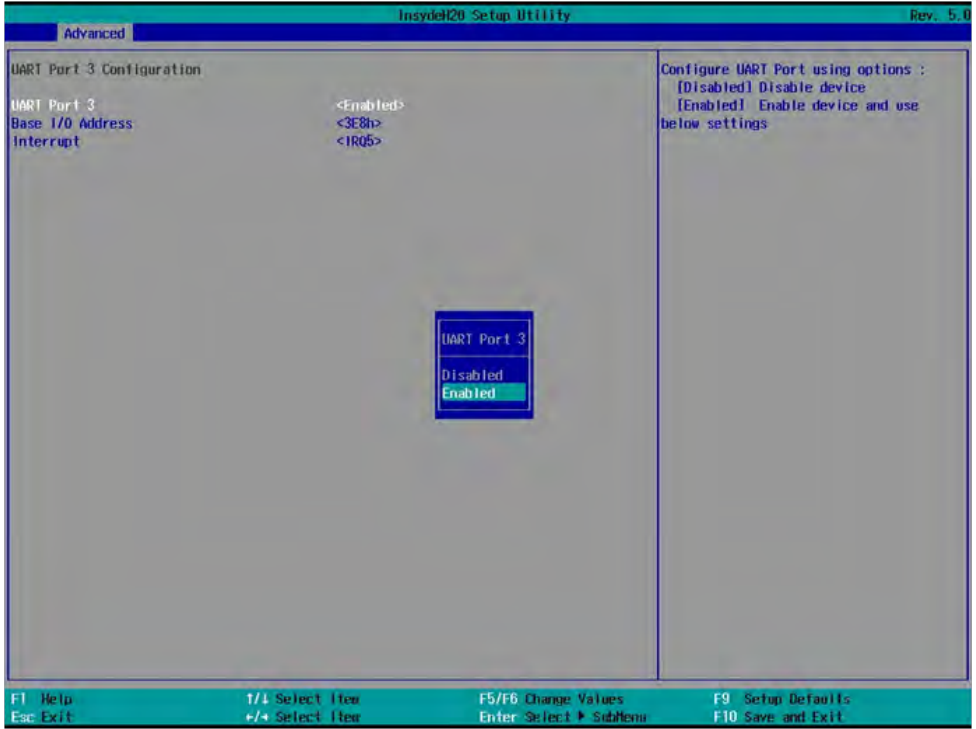
Base I/O Address, default is 2F8h.



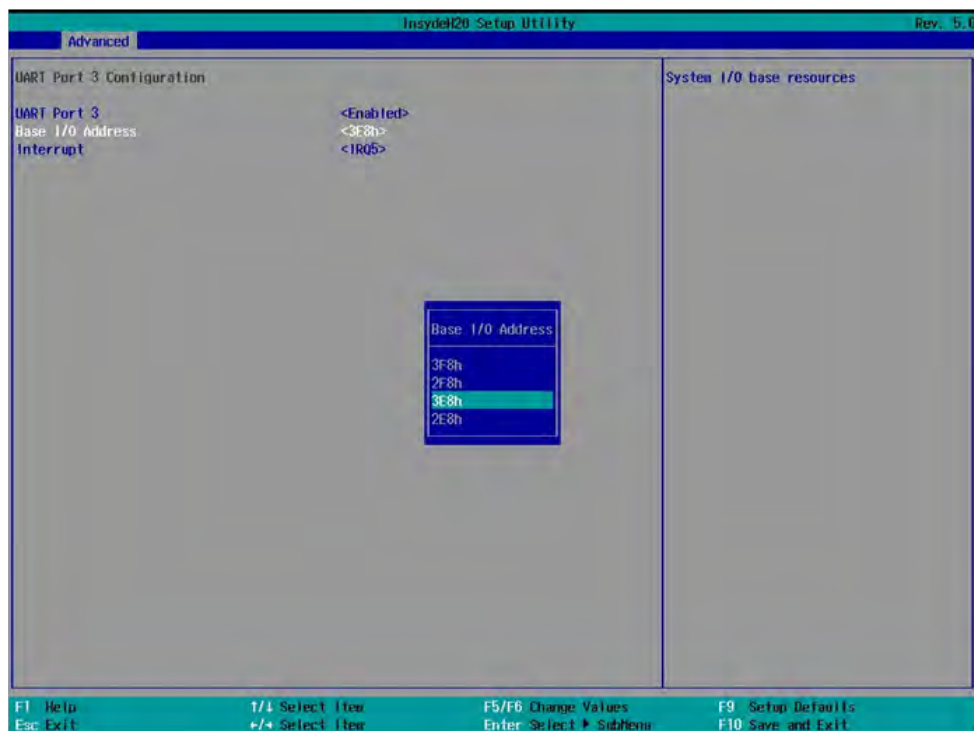


Interrupt, default is IRQ3.

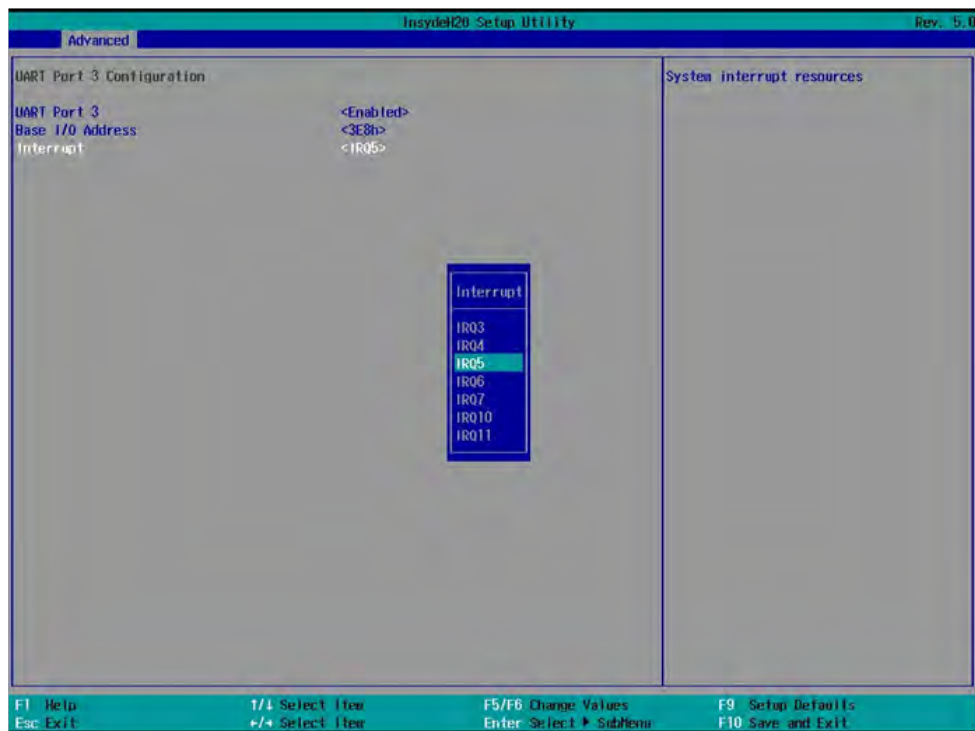
### 4-6-3-3 ► UART Port 3 Configuration



To Enable Serial port or not, default is Enabled.

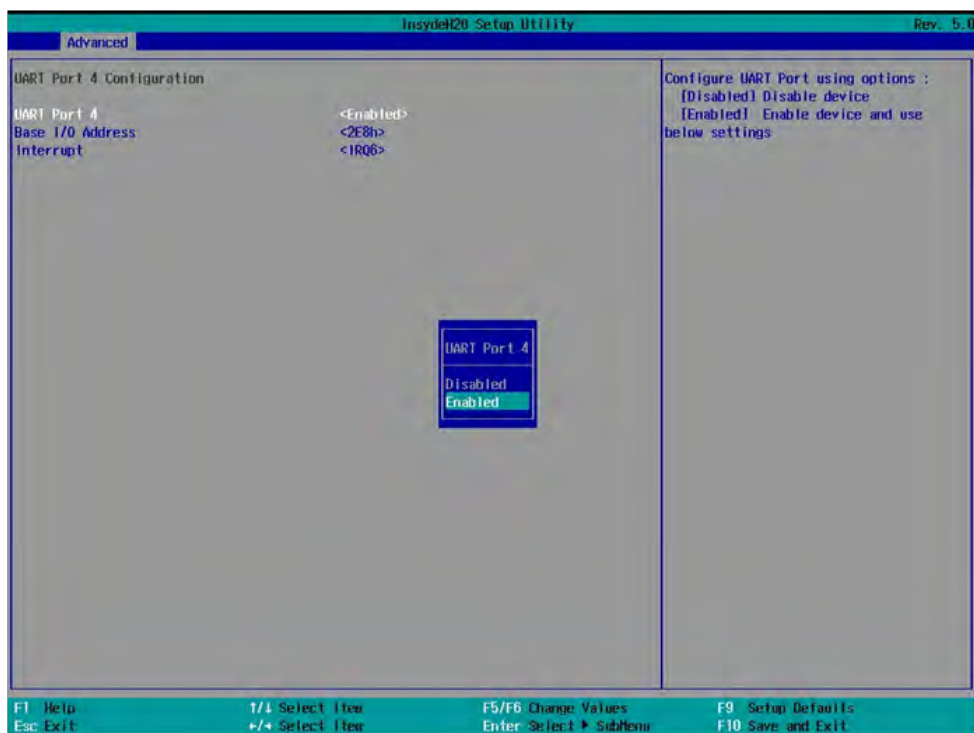


Base I/O Address, default is 3E8h.

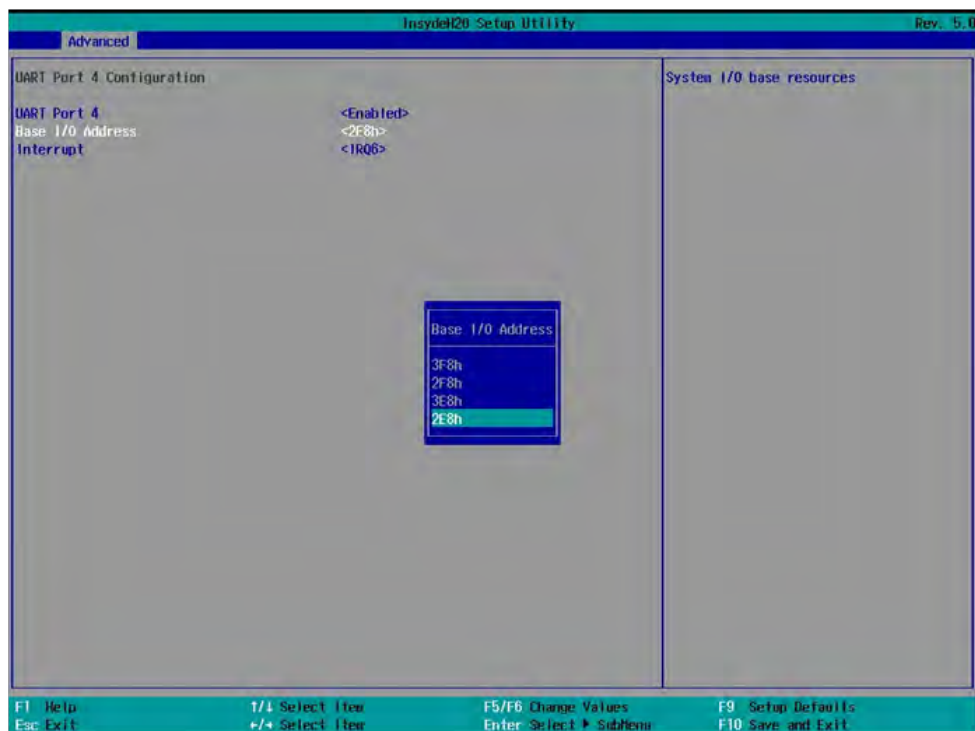


Interrupt, default is IRQ5.

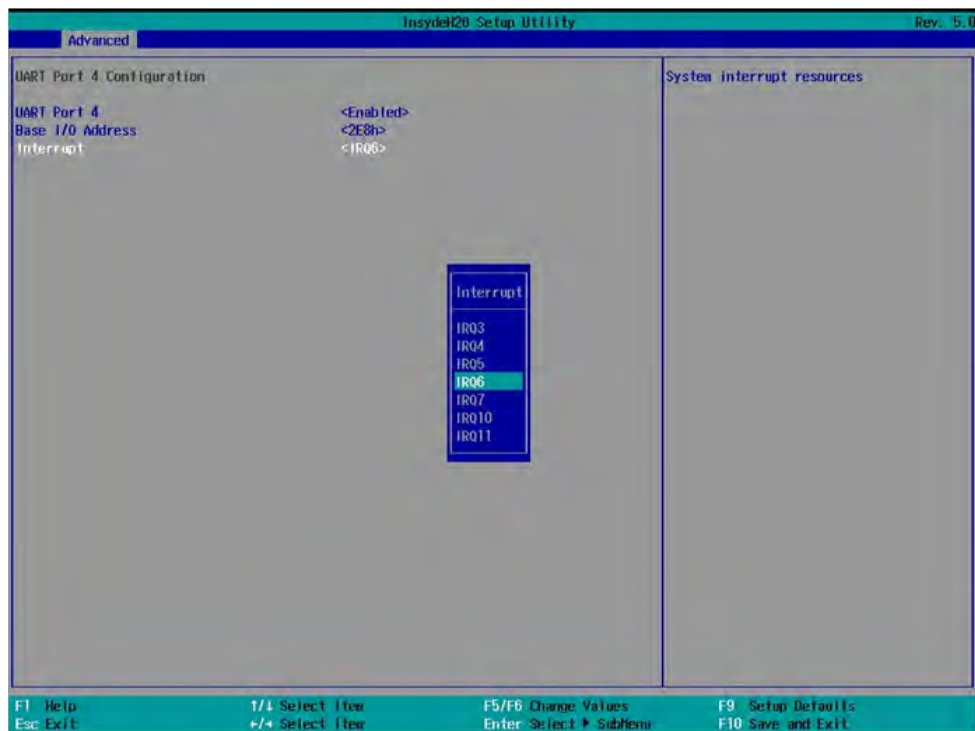
## 4-6-3-4 ► UART Port 4 Configuration



To Enable Serial port or not, default is Enabled.

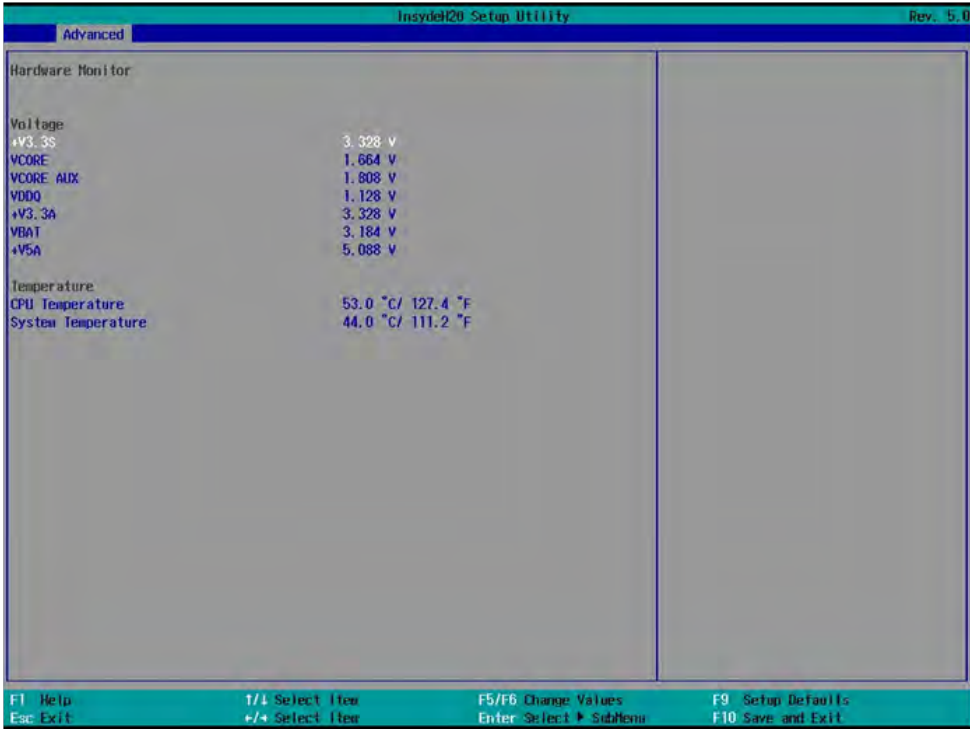


Base I/O Address, default is 2E8h.



Interrupt, default is IRQ6.

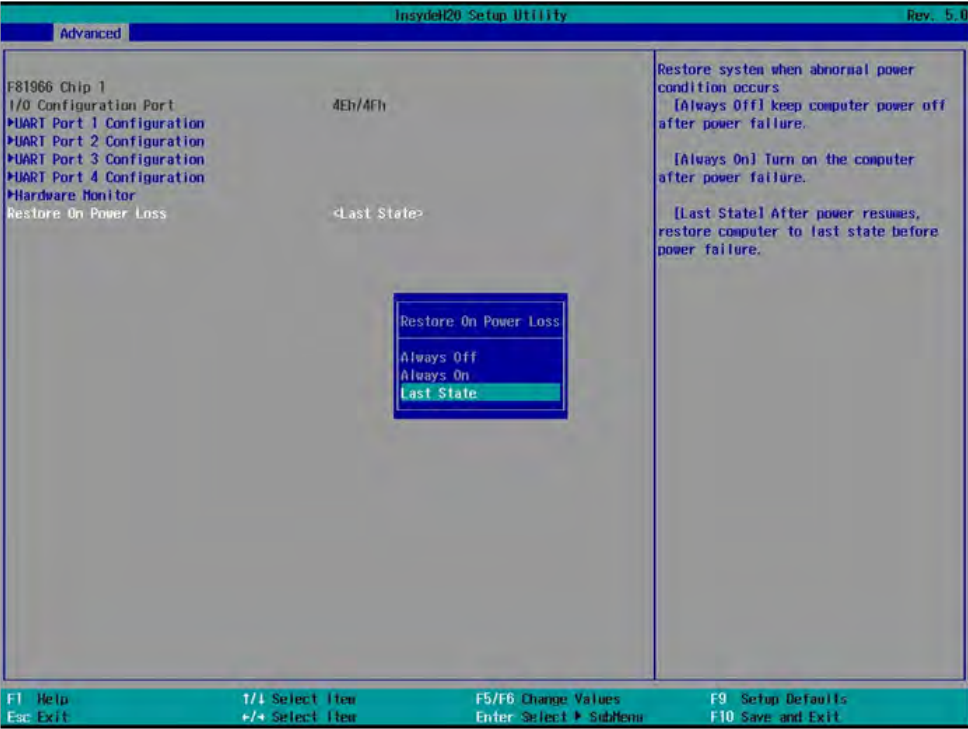
### 4-6-3-5 ► Hardware Monitor



Press [Enter] to view PC health status.  
This section shows the status of your CPU, Fan, and overall system.  
This is only available when there is Hardware Monitor function onboard.

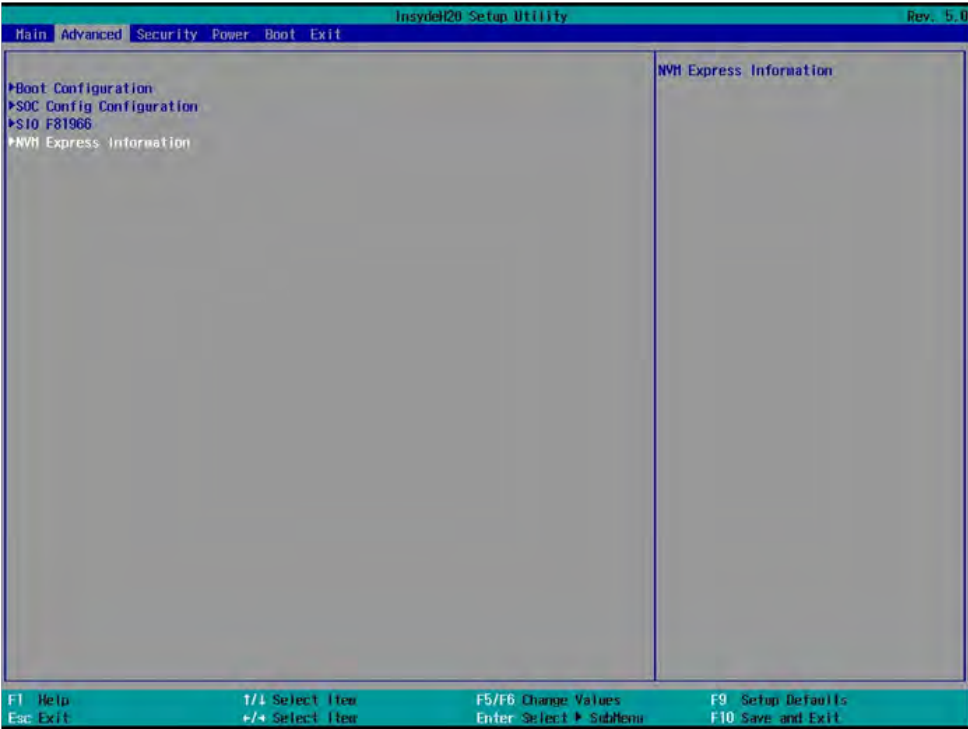


### 4-6-3-6 Restore On Power Loss



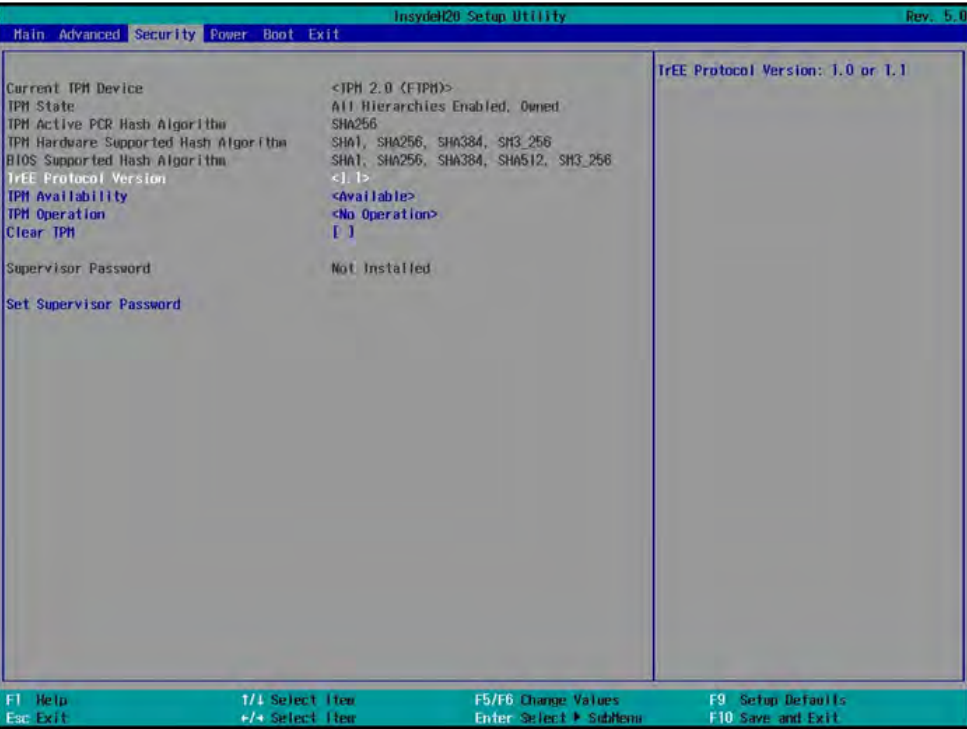
To select the power behavior after power fail, default is last state.

## 4-6-4 NVM Express Information



Press [Enter] to view the NVMe storage devices information.

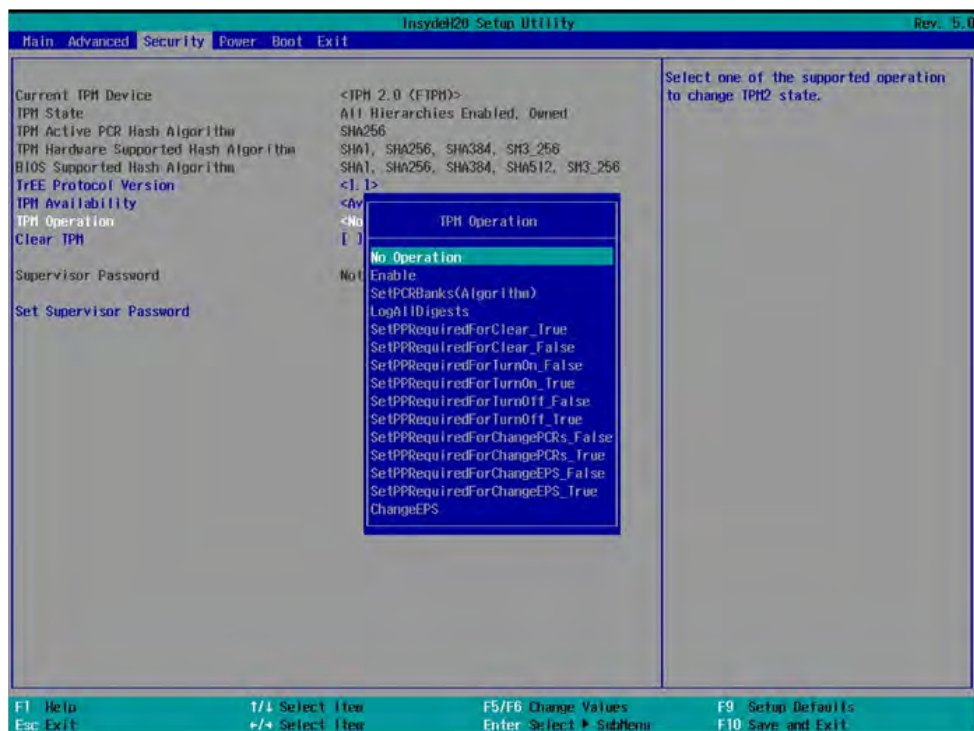
# 4-7 Security



**TrEE Protocol Version**  
There are 1.0 and 1.1 versions.

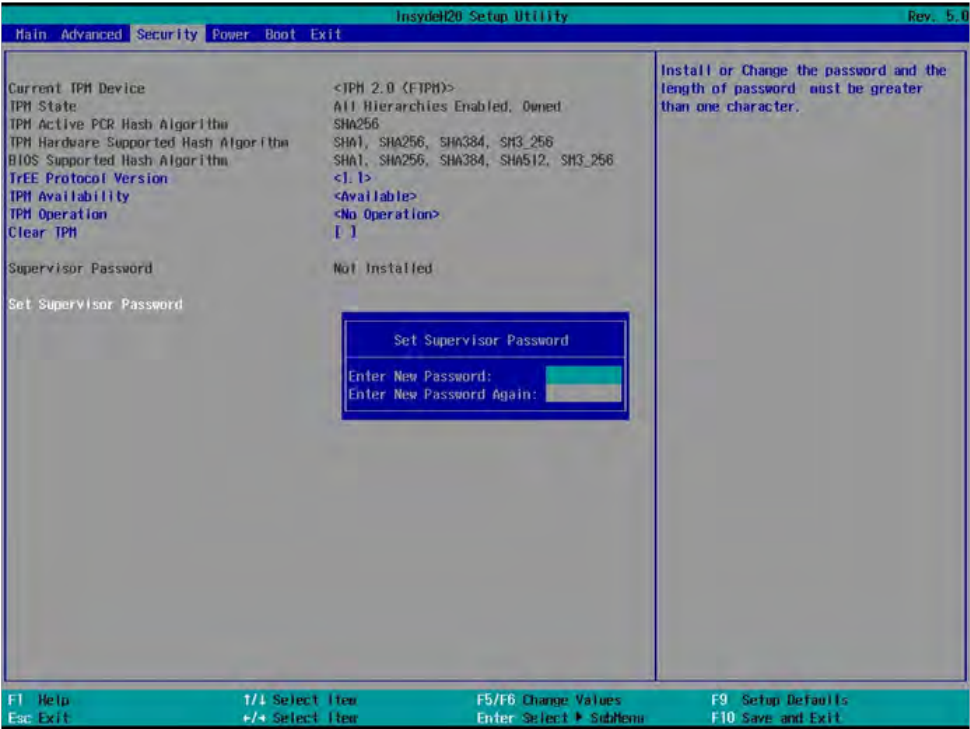
**TPM Availability**  
To select TPM available or hidden

**TPM Operation**



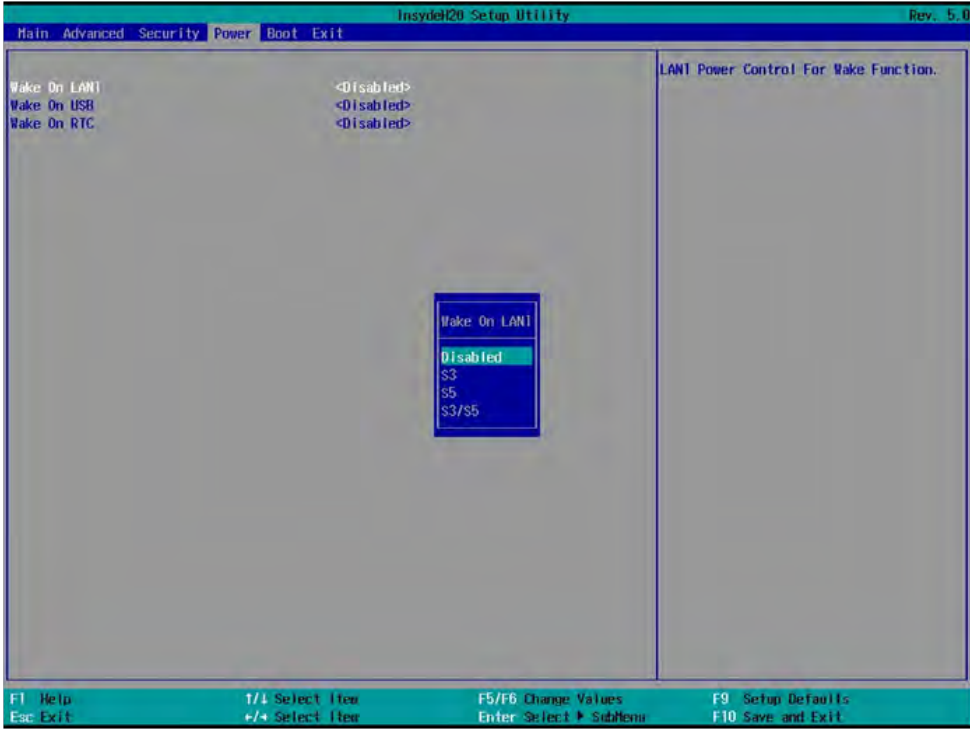
To select TPM operations

## Set Supervisor Password



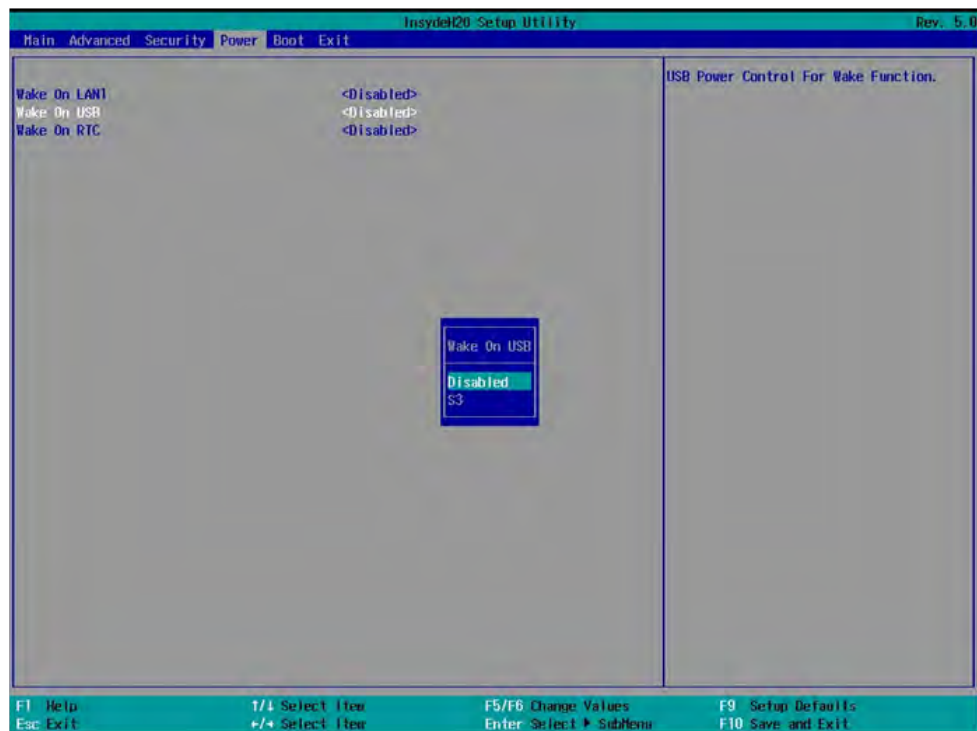
To set up an Supervisor password

# 4-8 Power



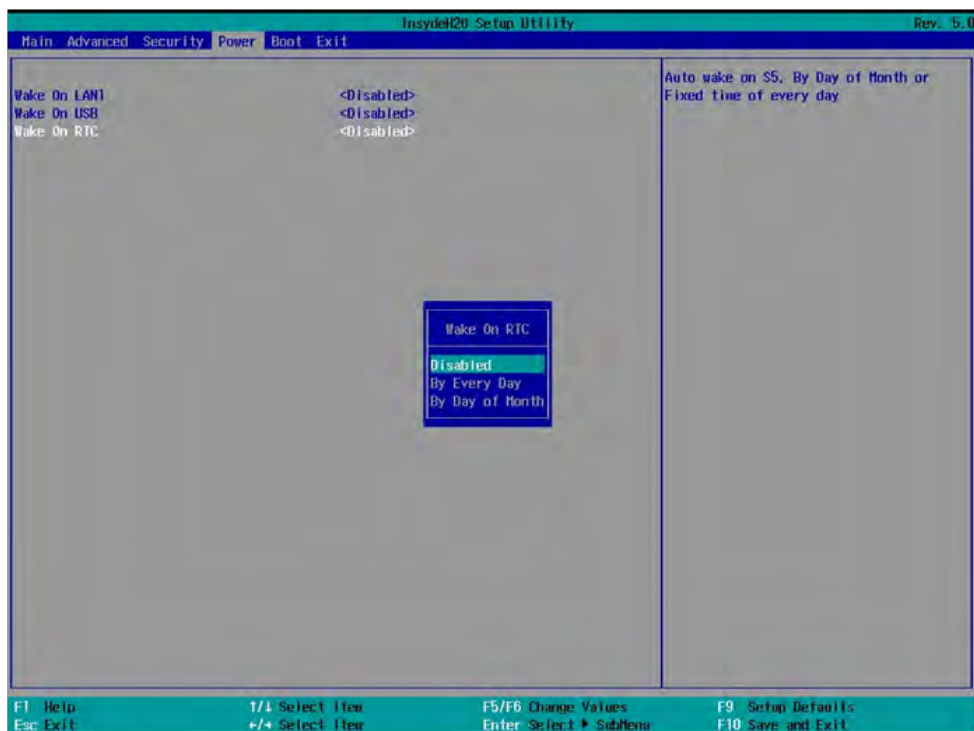
## Wake On LAN1

To select S3, S5 or S3/S5 wake on LAN1, default is Disabled.



## Wake On USB

To select S3 wake on USB, default is Disabled.

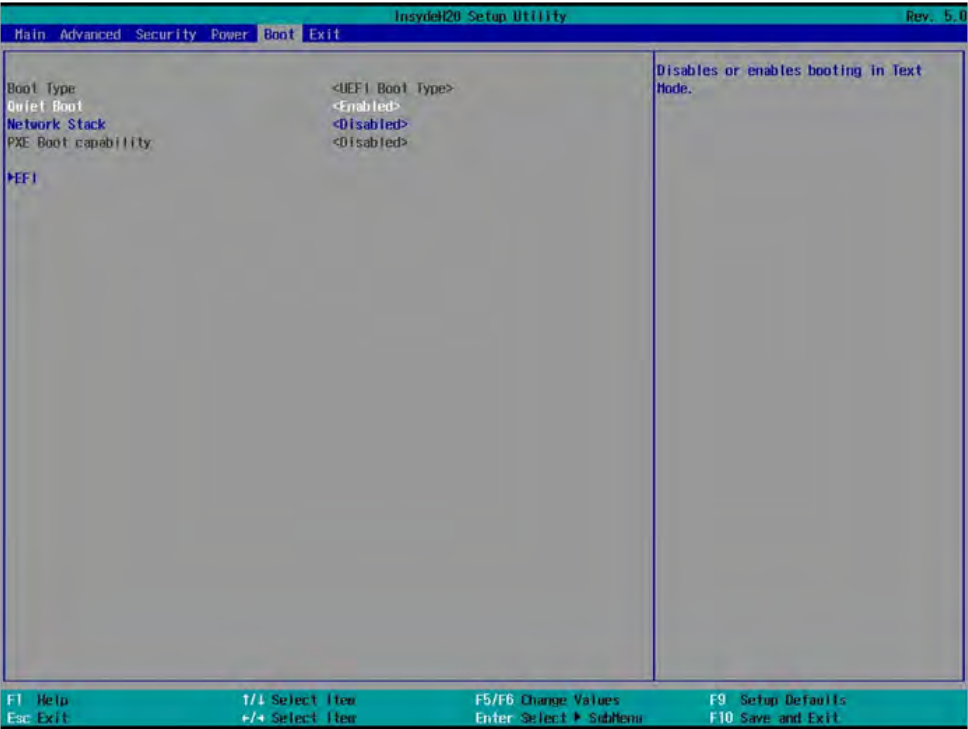


## Wake On RTC

The optional settings are: Disabled (default), By every day, By day of month.



# 4-9 Boot



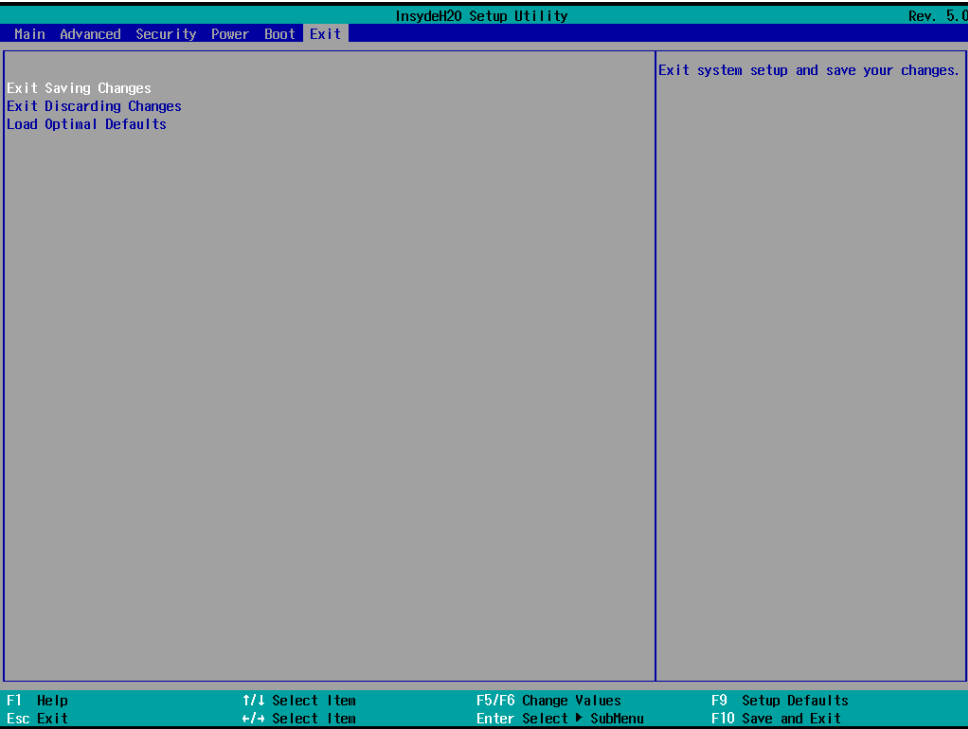
## Quiet Boot

The optional settings are: Enabled (default), Disabled.

## Network Stack

The optional settings are: Enabled, Disabled (default).

# 4-10 Save & Exit



**Exit Saving Changes**  
Save configuration and reset

**Exit Discarding Changes**  
Reset without saving the changes

**Load Optimal Defaults**  
To restore the optimal default for all the setup options

## 4-11 How to update Insyde BIOS

Under DOS Mode

STEP 1. Prepare a bootable disc.

(Storage device could be USB FDD or USB pen drive.)

STEP 2. Copy utility program to your bootable disc. You may download it from our website.

STEP 3. Copy the latest BIOS for your LEX motherboard from our website to your bootable disc.

STEP 4. (Here take 2I640SW as an example, please enter your motherboard's name)

Insert your bootable disc into X: (X could be C:, A: or others.

It depends on which type of storage device you use. )

Start the computer and type

X:\: H2OFFT-D.EXE 2I640SWA2.ROM -BIOS -ALL

2I640SWA2.ROM is the file name of the latest BIOS.

It may be 2I640SWA1.ROM or 2I640SWA2.ROM, etc.

Please leave one space between .ROM & -BIOS -ALL

By Bay Trail series mainboard, please type

X:\: H2OFFT-D.EXE 2I640SWA2.ROM -BIOS -ALL

-BIOS : Flash BIOS region

-ALL : Flash all

STEP 5. Press ENTER and the BIOS will be updated,  
Computer will restart automatically.

## Appendix B: Resolution list

640 x 480 x ( 256 / 16bit / 32bit )
800 x 600 x ( 256 / 16bit / 32bit )
1024 x 768 x ( 256 / 16bit / 32bit )
1152 x 864 x ( 256 / 16bit / 32bit )
1280 x 600 x ( 256 / 16bit / 32bit )
1280 x 720 x ( 256 / 16bit / 32bit )
1280 x 768 x ( 256 / 16bit / 32bit )
1280 x 800 x ( 256 / 16bit / 32bit )
1280 x 960 x ( 256 / 16bit / 32bit )
1280 x 1024 x ( 256 / 16bit / 32bit )
1400 x 1050 x ( 256 / 16bit / 32bit )
1440 x 900 x ( 256 / 16bit / 32bit )
1600 x 900 x ( 256 / 16bit / 32bit )
1600 x 1200 x ( 256 / 16bit / 32bit )
1680 x 1050 x ( 256 / 16bit / 32bit )
1920 x 1080 x ( 256 / 16bit / 32bit )
1920 x 1200 x ( 256 / 16bit / 32bit )